

# PFS

## BIDIRECTIONAL TRIGGER DIODE

**DB4**

**REVERSE VOLTAGE  
POWER**

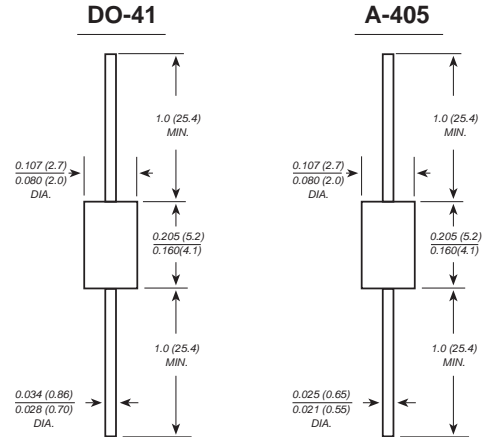
**40 VOLTS  
150 mW**

### FEATURES

- The plastic package
- VBO:35-45V version
- Low breakover current
- High temperature soldering guaranteed  
260°C/10 seconds,0.375" (9.5mm) lead length,  
5 lbs. (2.3kg) tension

### MECHANICAL DATA

- Case : JEDEC DO-41/A-405 plastic body
- Terminals : Plated axial leads, solderable per MIL-STD-750,  
Method 2026
- Mounting Position : Any
- Weight :DO-41 0.012 ounce, 0.33gram  
A-405 0.008 ounce, 0.23gram



Dimensions in inches and (millimeters)

### MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

- Ratings at 25°C ambient temperature unless otherwise specified.

	TEST CONDITION	SYMBOLS	VALUE			UNITS
			Min.	Typ.	Max.	
Breakover voltage *	C=22nF **	$V_{BO}$	35	40	45	VOLTS
Breakover voltage symmetry	C=22nF **	$ +V_{BO1}-I-V_{BO} $	-3		3	VOLTS
Dynamic breakover voltage *	(NOTE 1)	$  \Delta V \pm I  $	5			VOLTS
Output voltage *	DIAGRAM2	$V_o$	5			VOLTS
Breakover current *	C=22nF **	$I_{bo}$			100	$\mu A$
Rise time *	DIAGRAM3	$t_r$		1.5		$\mu S$
Leakage current *	$V_R=0.5V_{BO}$	$I_B$			10	$\mu A$
Power dissipation on printed circuit	$T_A=65^\circ C$	$P_d$			150	mW
Repetitive peak on-state current	$t_p=20\mu s$ $f=100Hz$	$I_{TRM}$			2	A
Thermal Resistances from Junction to ambient		$R_{\theta JA}$			400	$^\circ C/W$
Thermal Resistances from Junction to lead		$R_{\theta JL}$			150	$^\circ C/W$
Operating junction and storage temperature range		$T_J, T_{STG}$	-40		125	$^\circ C$

- :Electrical characteristic applicable in forward and reverse directions.
- :Connected in parallel with the devices.

Note 1:  $I_{bo}$  from  $I_{bo}$  to 10mA

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DIAGRAM 1: CURRENT-VOLTAGE CHARACTERISTICS

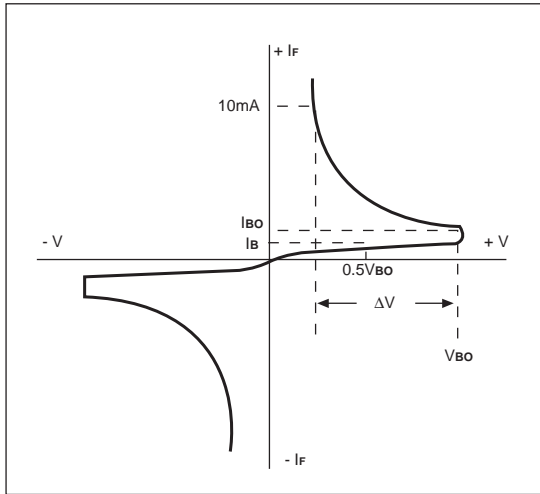


FIG. 1-POWER DISSIPATION VERSUS AMBIENT TEMPERATURE(MAXIMUM VALUES)

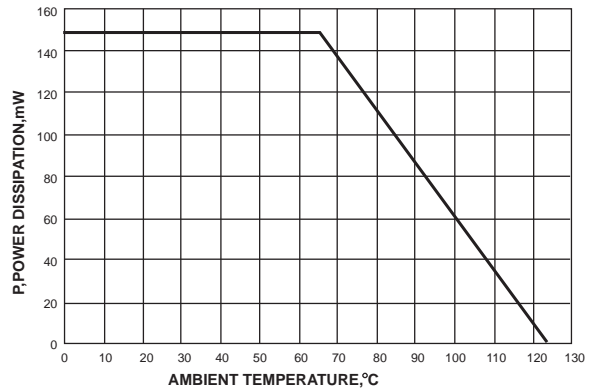


FIG. 2-PEAK PULSE CURRENT VERSUS PULSE DURATION (MAXIMUM VALUES)

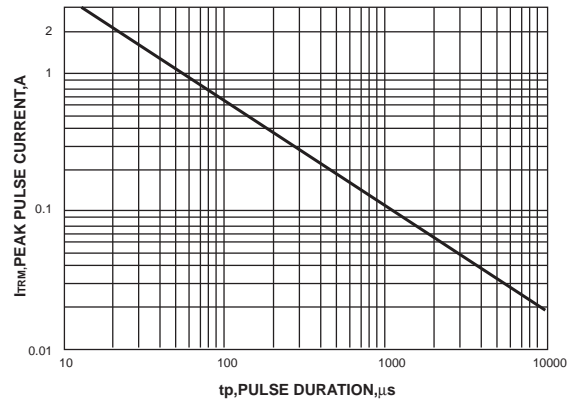


FIG. 3-RELATIVE VARIATION OF VBo VERSUS JUNCTION TEMPERATURE(TYPICAL VALUES)

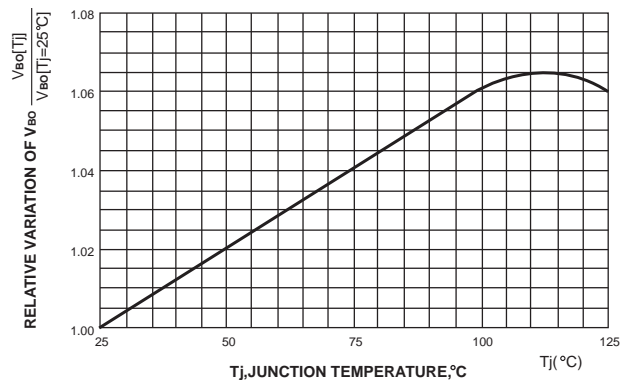


DIAGRAM 2: TEST CIRCUIT OUTPUT VOLTAGE

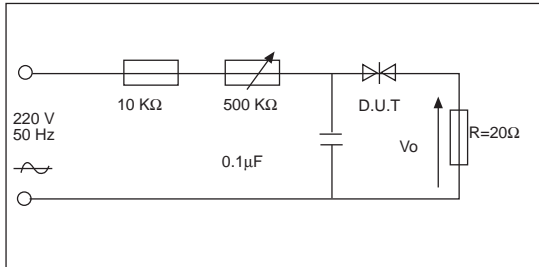


DIAGRAM 3: TEST CIRCUIT SEE DIAGRAM 2. ADJUST R FOR IP=0.5A

