

BYM36 series

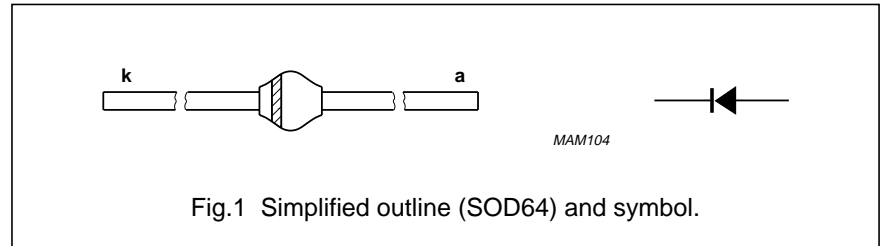
FEATURES

- Glass passivated
- High maximum operating temperature
- Low leakage current
- Excellent stability
- Guaranteed avalanche energy absorption capability
- Available in ammo-pack
- Also available with preformed leads for easy insertion.

DESCRIPTION

Rugged glass SOD64 package, using a high temperature alloyed construction.

This package is hermetically sealed and fatigue free as coefficients of expansion of all used parts are matched.



LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{RRM}	repetitive peak reverse voltage				
	BYM36A		–	200	V
	BYM36B		–	400	V
	BYM36C		–	600	V
	BYM36D		–	800	V
	BYM36E		–	1000	V
	BYM36F BYM36G		–	1200 1400	V V
V _R	continuous reverse voltage				
	BYM36A		–	200	V
	BYM36B		–	400	V
	BYM36C		–	600	V
	BYM36D		–	800	V
	BYM36E		–	1000	V
	BYM36F BYM36G		–	1200 1400	V V
I _{F(AV)}	average forward current	T _{tp} = 55 °C; lead length = 10 mm; see Figs 2; 3 and 4	–	3.0	A
	BYM36A to C		–	2.9	A
	BYM36D and E BYM36F and G	averaged over any 20 ms period; see also Figs 14; 15 and 16	–	2.9	A
I _{F(AV)}	average forward current	T _{amb} = 65 °C; PCB mounting (see Fig.25); see Figs 5; 6 and 7	–	1.25	A
	BYM36A to C		–	1.20	A
	BYM36D and E BYM36F and G	averaged over any 20 ms period; see also Figs 14; 15 and 16	–	1.15	A



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SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _{FRM}	repetitive peak forward current	T _{tp} = 55 °C; see Figs 8; 9 and 10	–	37	A
	BYM36A to C		–	33	A
	BYM36D and E BYM36F and G		–	27	A
I _{FRM}	repetitive peak forward current	T _{amb} = 65 °C; see Figs 11; 12 and 13	–	13	A
	BYM36A to C		–	11	A
	BYM36D and E BYM36F and G		–	10	A
I _{FSM}	non-repetitive peak forward current	t = 10 ms half sine wave; T _j = T _{j max} prior to surge; V _R = V _{RRMmax}	–	65	A
E _{RSM}	non-repetitive peak reverse avalanche energy	L = 120 mH; T _j = T _{j max} prior to surge; inductive load switched off	–	10	mJ
T _{stg}	storage temperature		–65	+175	°C
T _j	junction temperature	see Figs 17 and 18	–65	+175	°C

ELECTRICAL CHARACTERISTICS

T_j = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V _F	forward voltage	I _F = 3 A; T _j = T _{j max} ; see Figs 19; 20 and 21	–	–	1.22	V
	BYM36A to C		–	–	1.28	V
	BYM36D and E BYM36F and G		–	–	1.24	V
V _F	forward voltage	I _F = 3 A; see Figs 19; 20 and 21	–	–	1.60	V
	BYM36A to C		–	–	1.78	V
	BYM36D and E BYM36F and G		–	–	1.57	V
V _{(BR)R}	reverse avalanche breakdown voltage	I _R = 0.1 mA				
	BYM36A		300	–	–	V
	BYM36B		500	–	–	V
	BYM36C		700	–	–	V
	BYM36D		900	–	–	V
	BYM36E		1100	–	–	V
	BYM36F BYM36G		1300 1500	–	–	V
I _R	reverse current	V _R = V _{RRMmax} ; see Fig.22	–	–	5	μA
		V _R = V _{RRMmax} ; T _j = 165 °C; see Fig.22	–	–	150	μA

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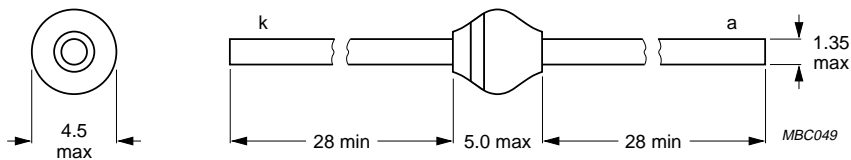
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
t_{rr}	reverse recovery time	when switched from				
	BYM36A to C	$I_F = 0.5 \text{ A}$ to $I_R = 1 \text{ A}$;	–	–	100	ns
	BYM36D and E	measured at $I_R = 0.25 \text{ A}$;	–	–	150	ns
	BYM36F and G	see Fig. 26	–	–	250	ns
C_d	diode capacitance	$f = 1 \text{ MHz}$; $V_R = 0 \text{ V}$;				
	BYM36A to C	see Figs 23 and 24	–	85	–	pF
	BYM36D and E		–	75	–	pF
	BYM36F and G		–	65	–	pF
$\left \frac{dI_R}{dt} \right $	maximum slope of reverse recovery current	when switched from				
	BYM36A to C	$I_F = 1 \text{ A}$ to $V_R \geq 30 \text{ V}$ and	–	–	7	A/ μs
	BYM36D and E	$dI_F/dt = -1 \text{ A}/\mu\text{s}$;	–	–	6	A/ μs
	BYM36F and G	see Fig.27	–	–	5	A/ μs

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th \text{ j-tp}}$	thermal resistance from junction to tie-point	lead length = 10 mm	25	K/W
$R_{th \text{ j-a}}$	thermal resistance from junction to ambient	note 1	75	K/W

Note

1. Device mounted on an epoxy-glass printed-circuit board, 1.5 mm thick; thickness of Cu-layer $\geq 40 \mu\text{m}$, see Fig.25. For more information please refer to the "General Part of associated Handbook".

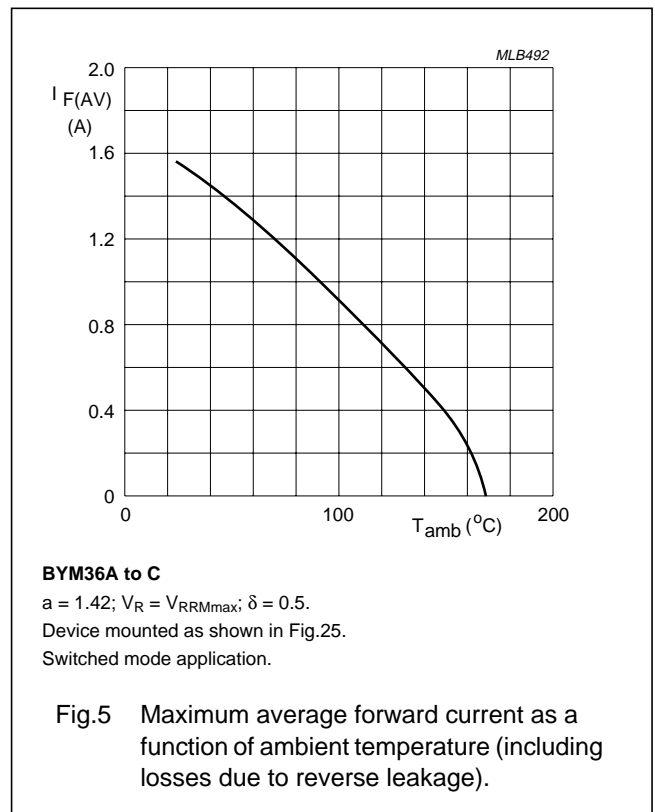
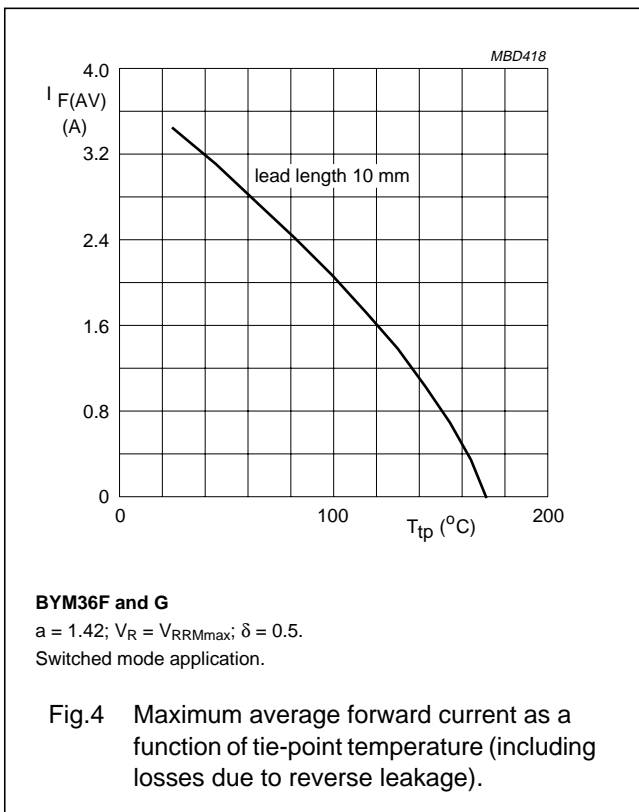
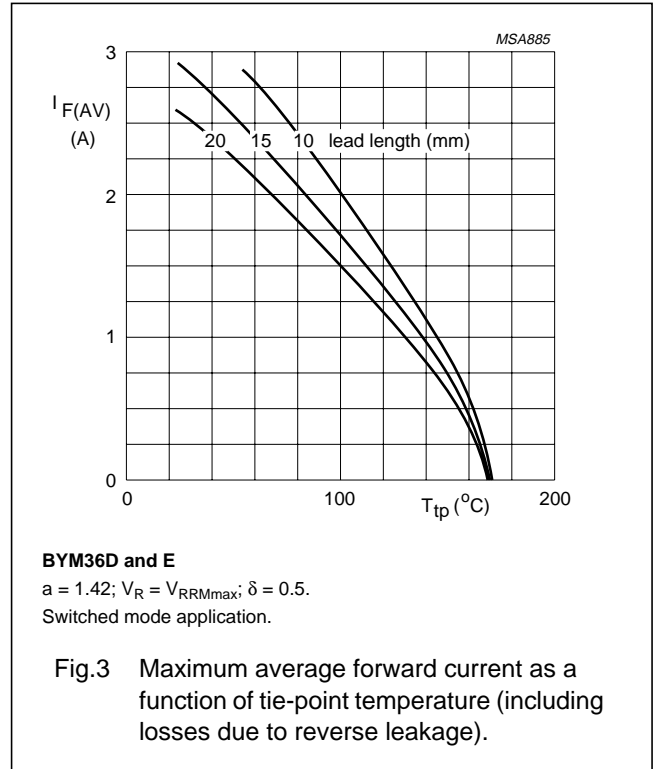
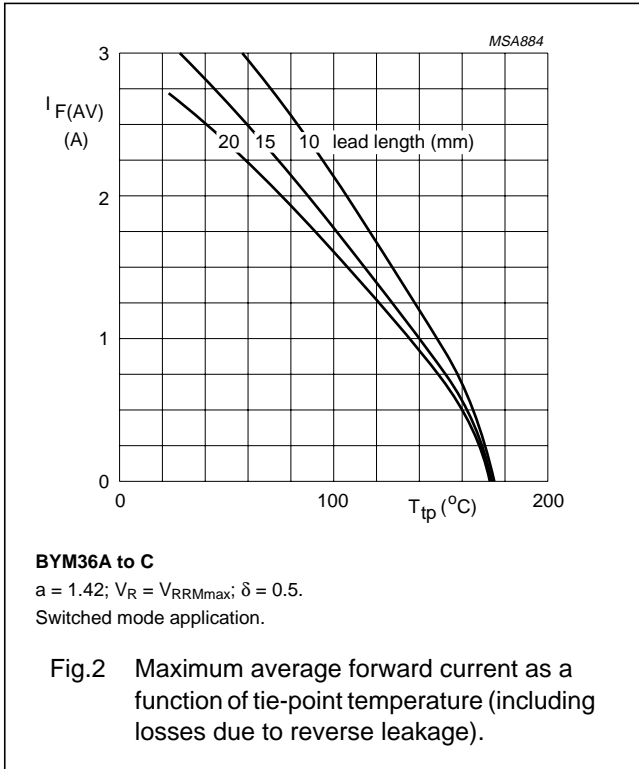


Dimensions in mm.
The marking band indicates the cathode.

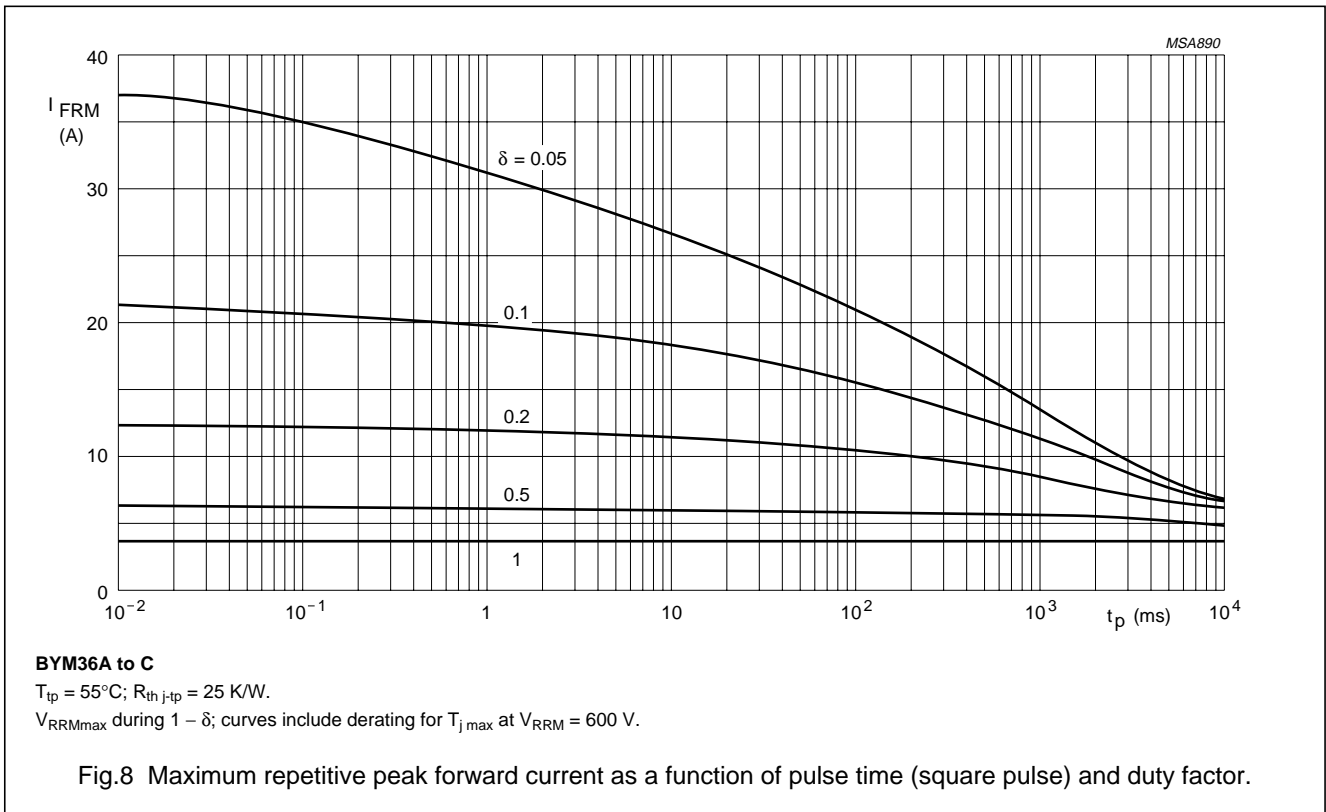
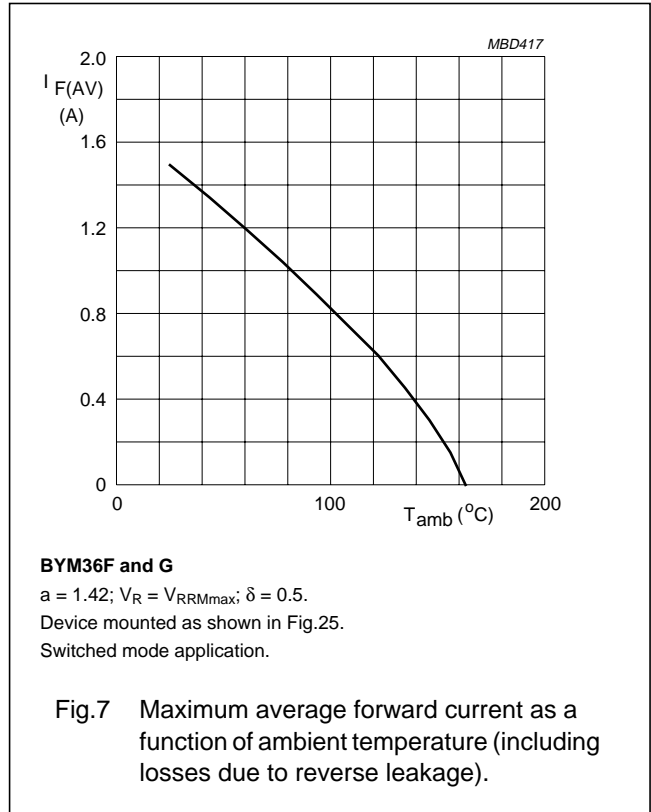
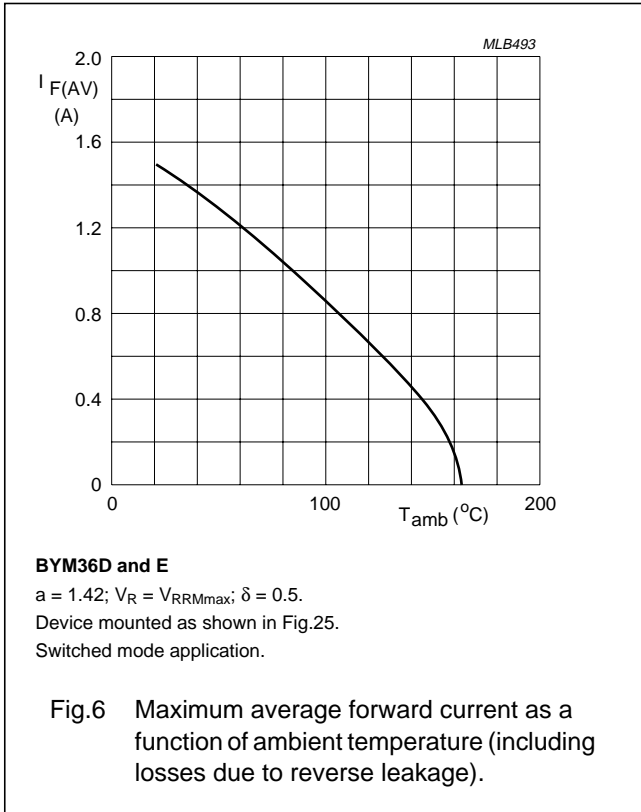
Fig.28 SOD64.

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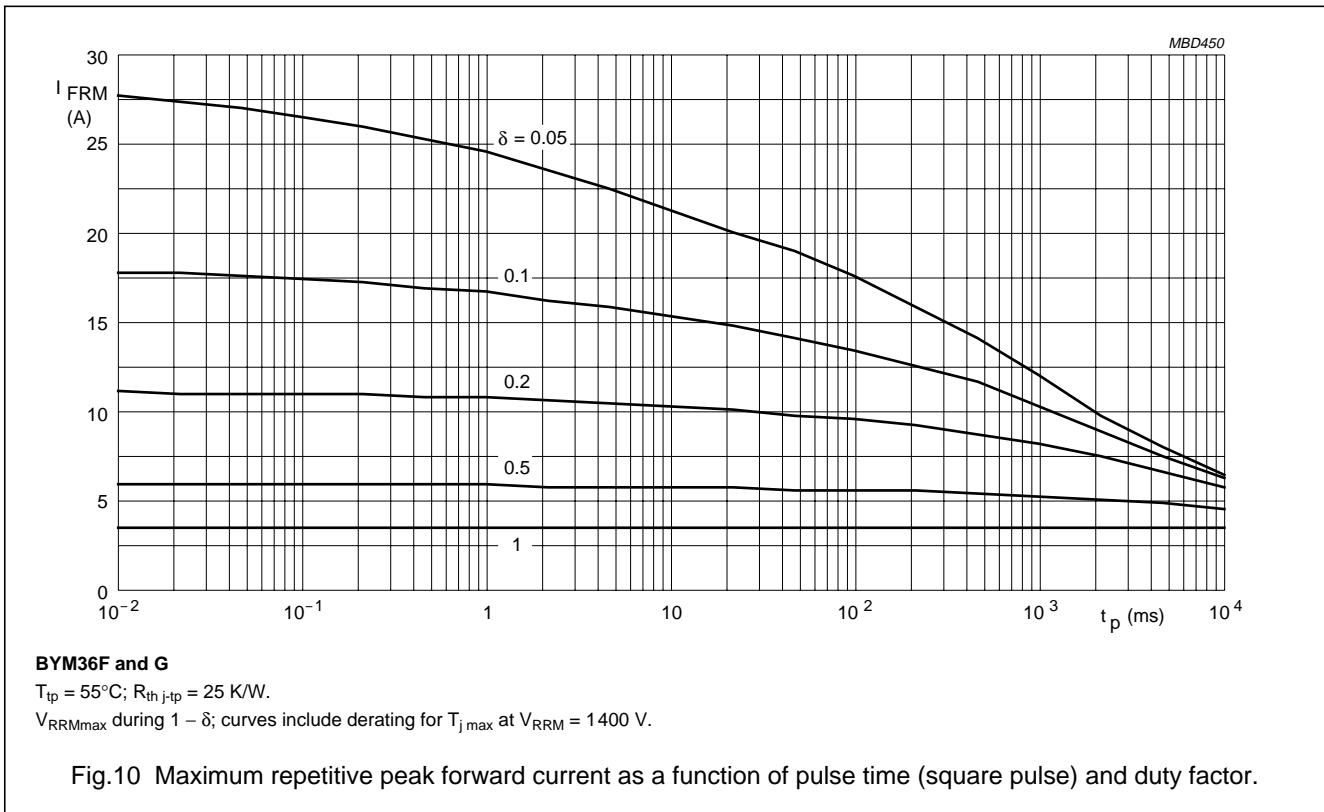
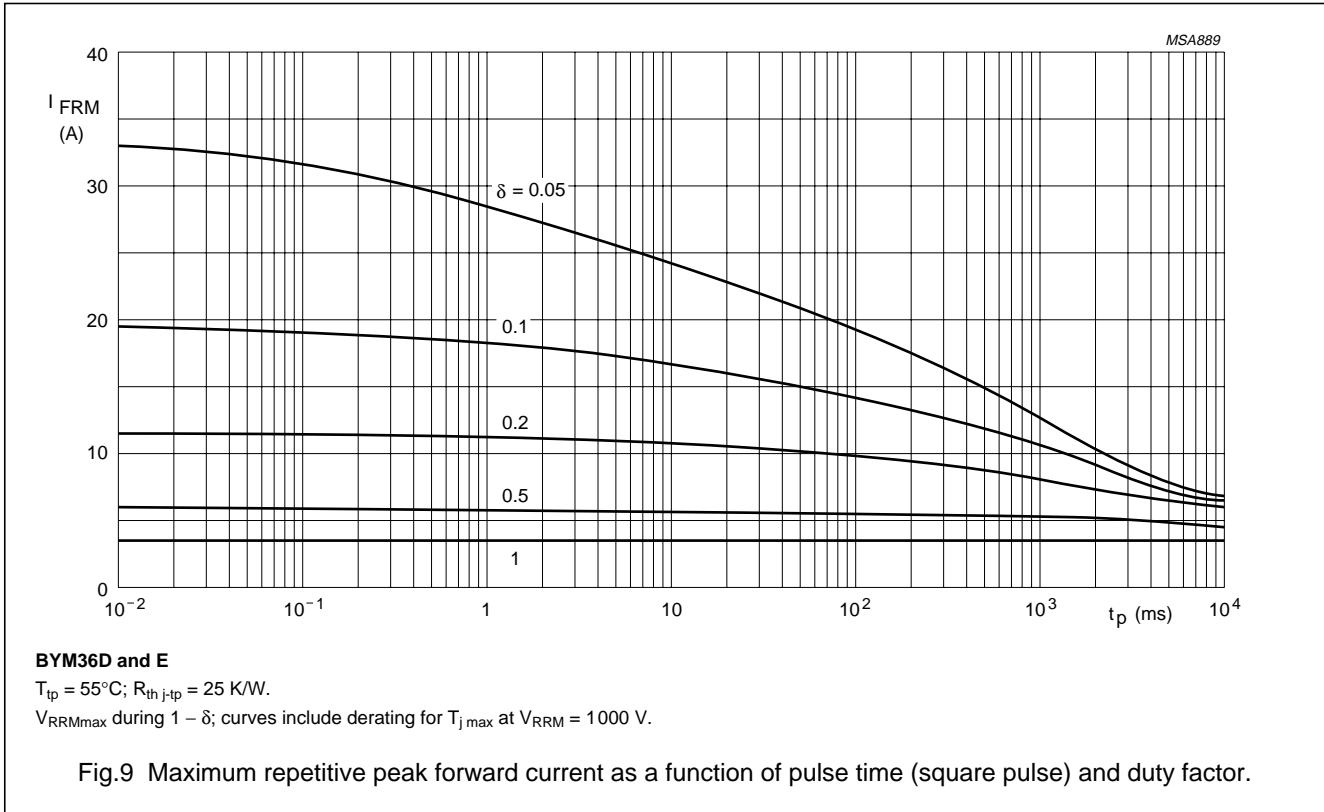
GRAPHICAL DATA



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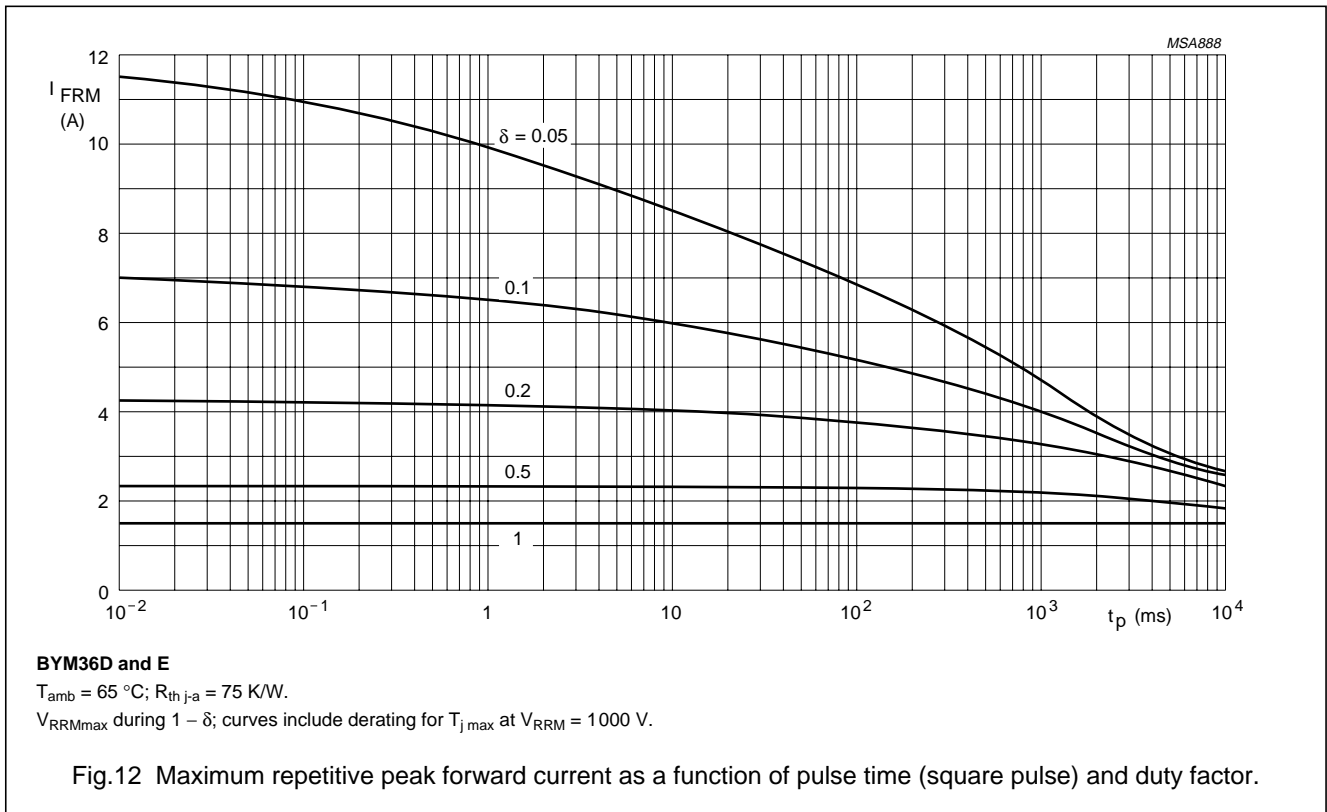
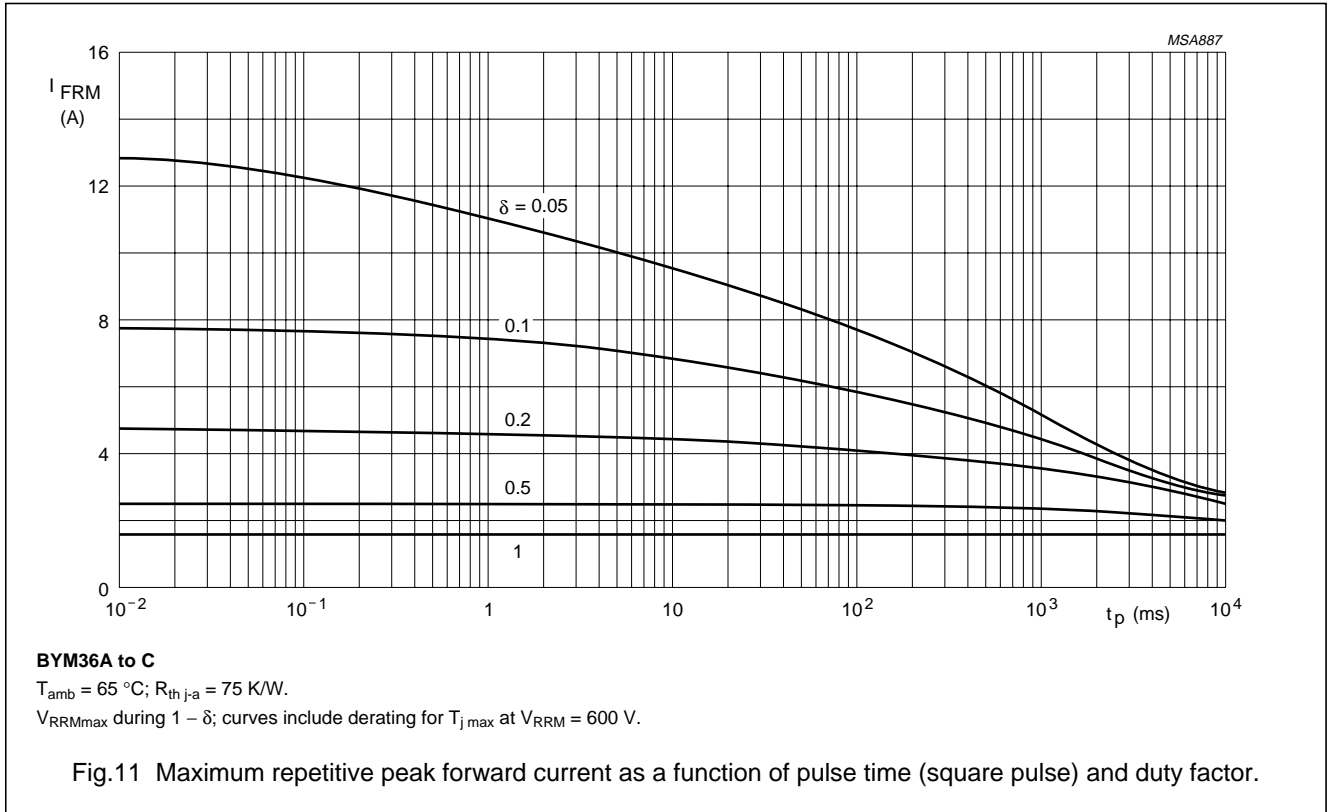
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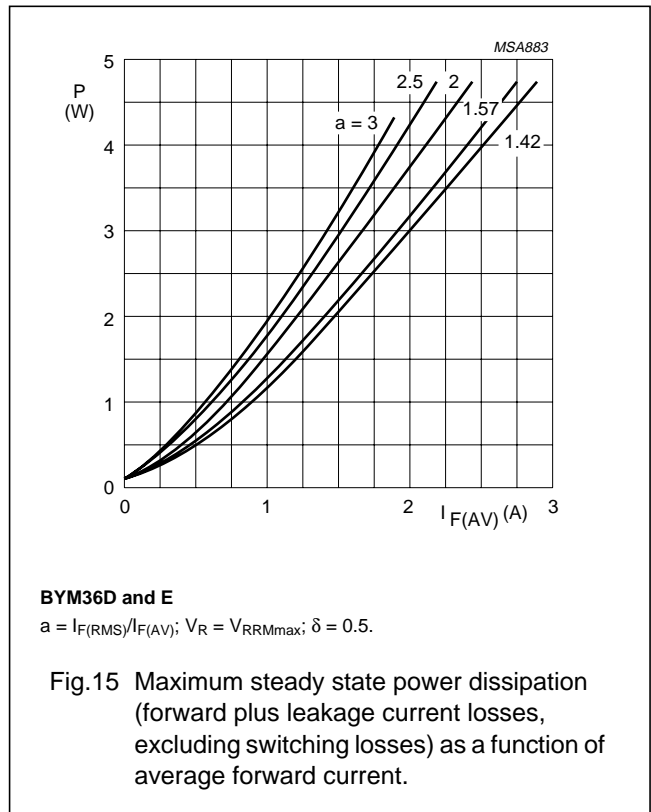
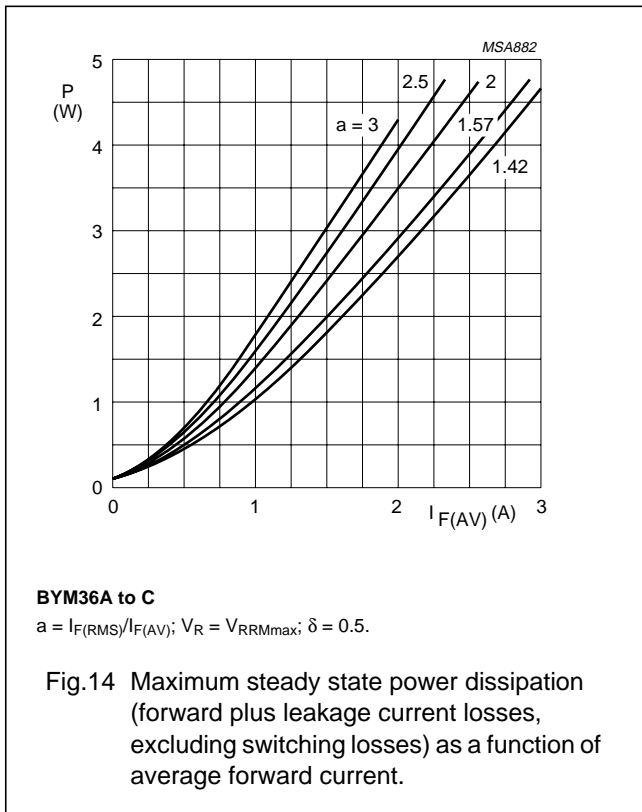
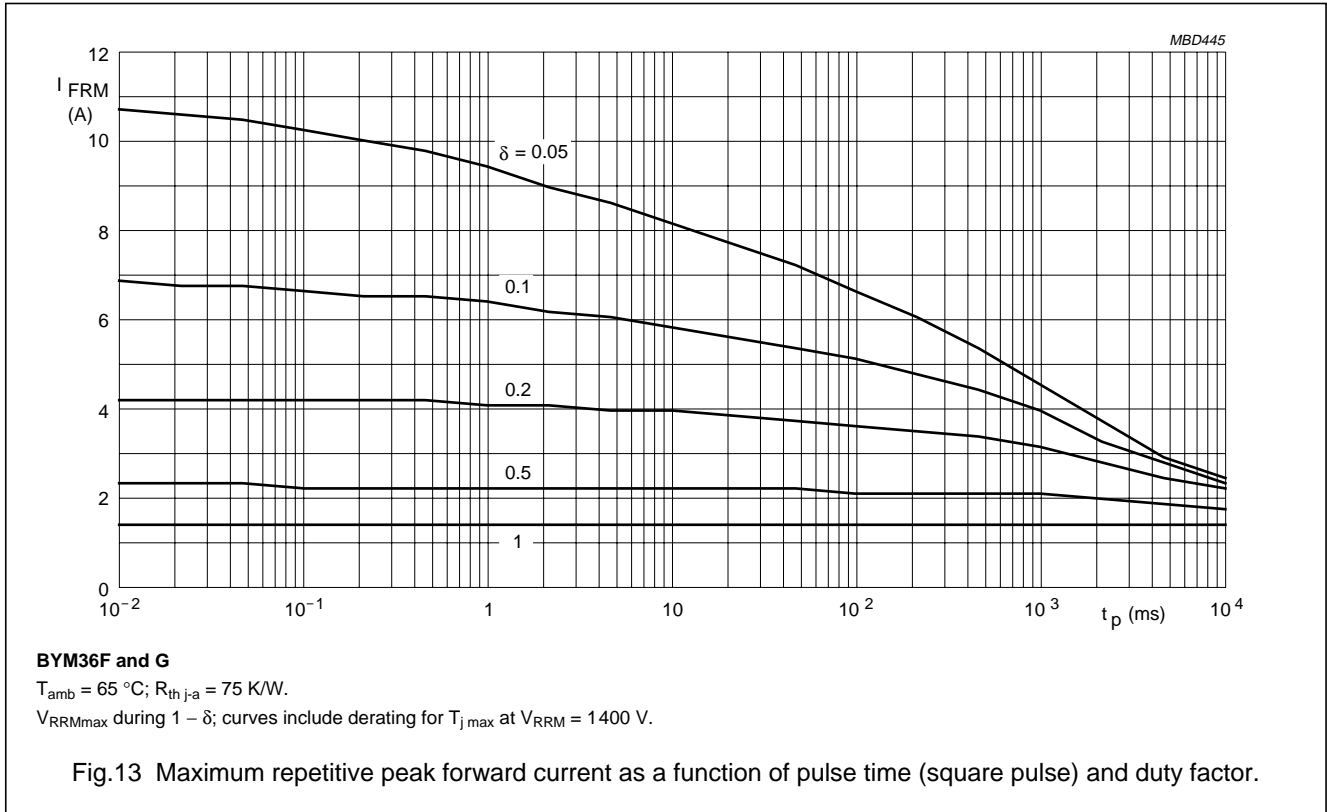


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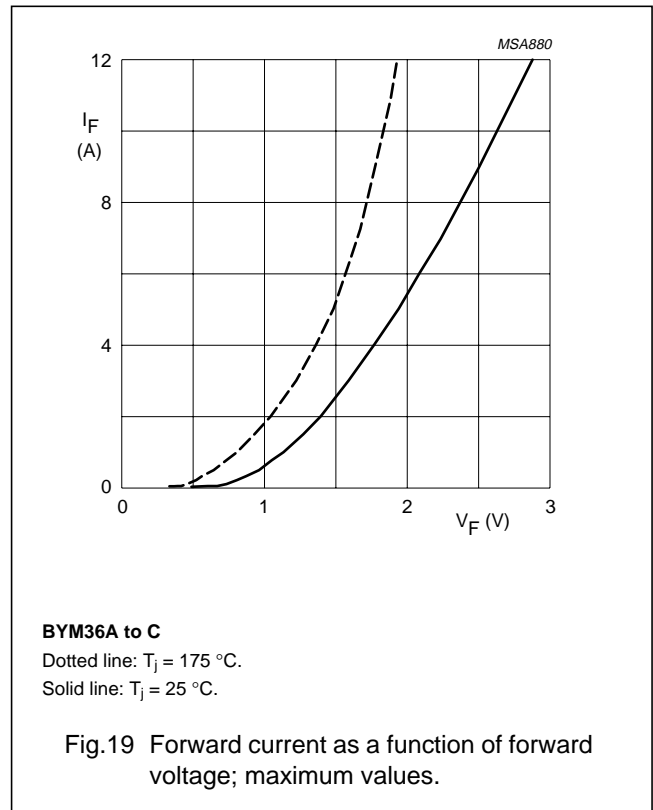
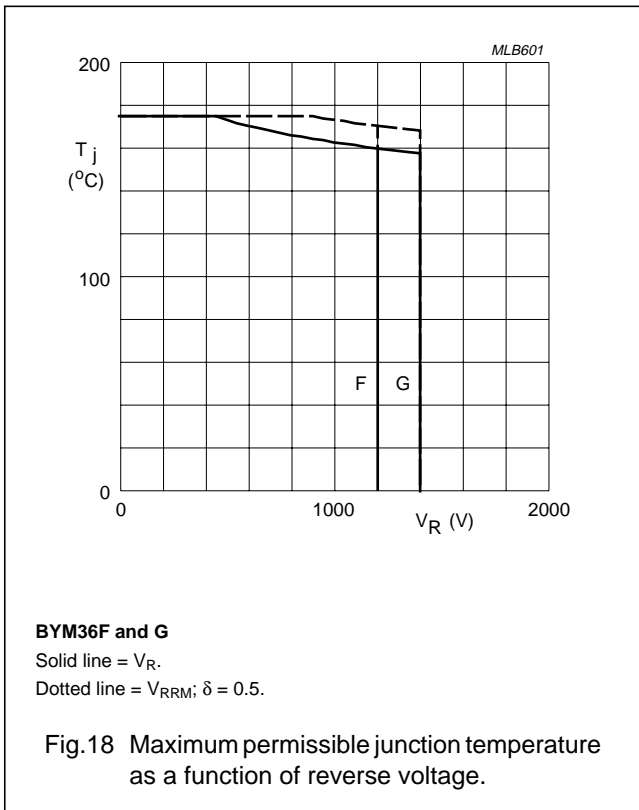
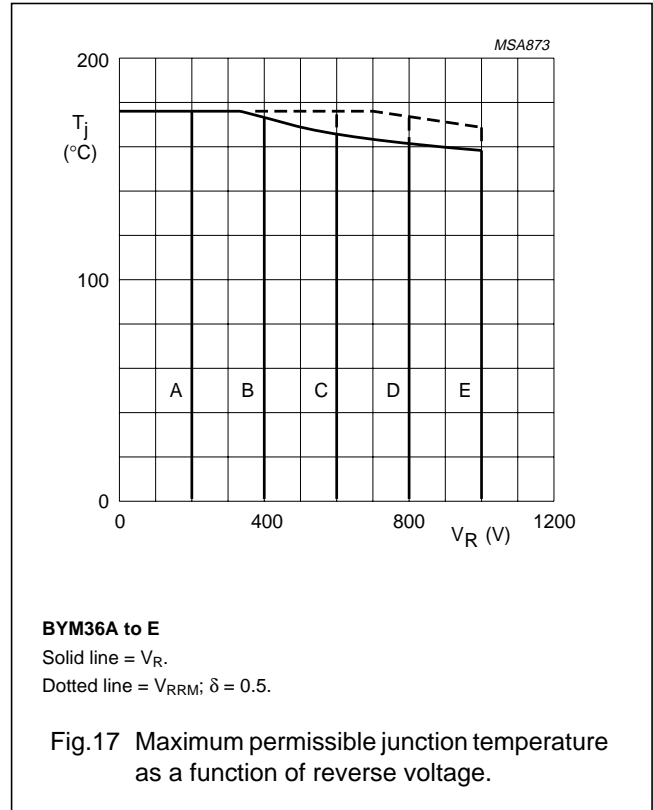
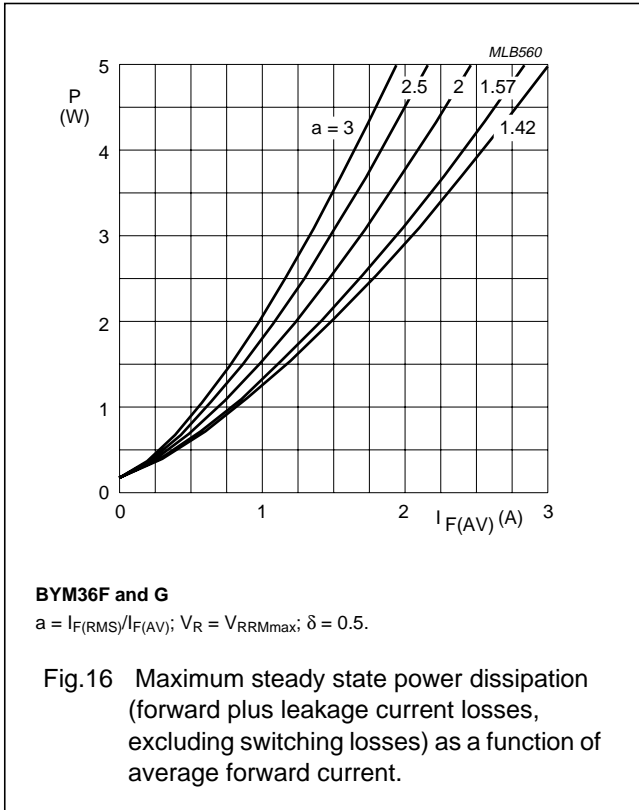
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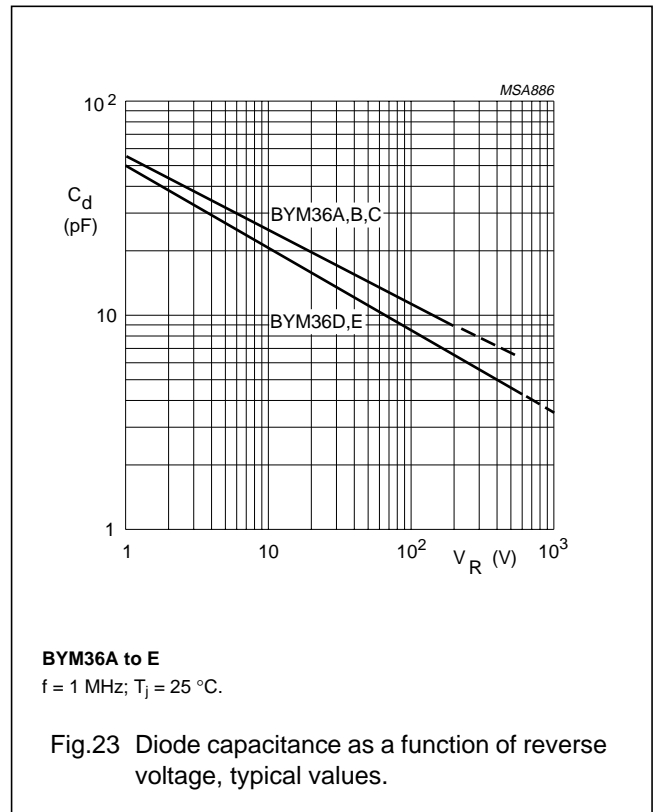
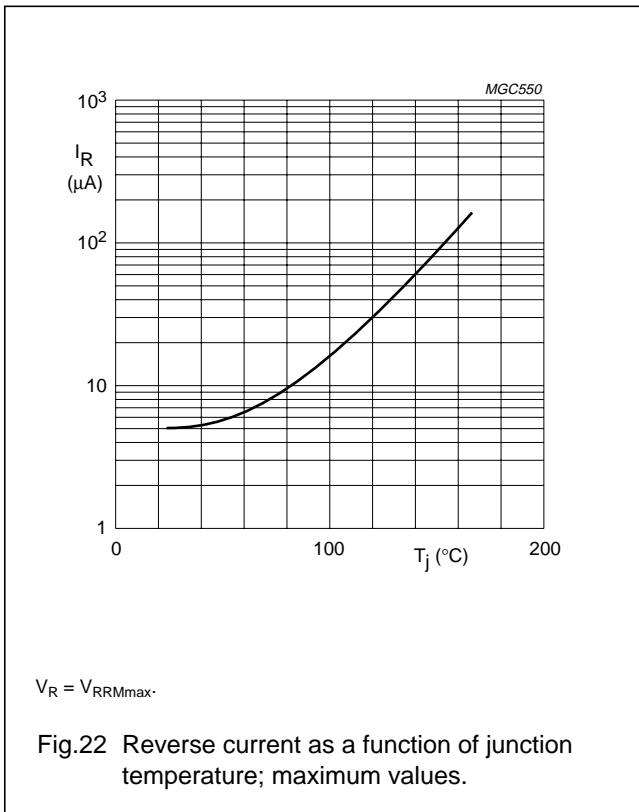
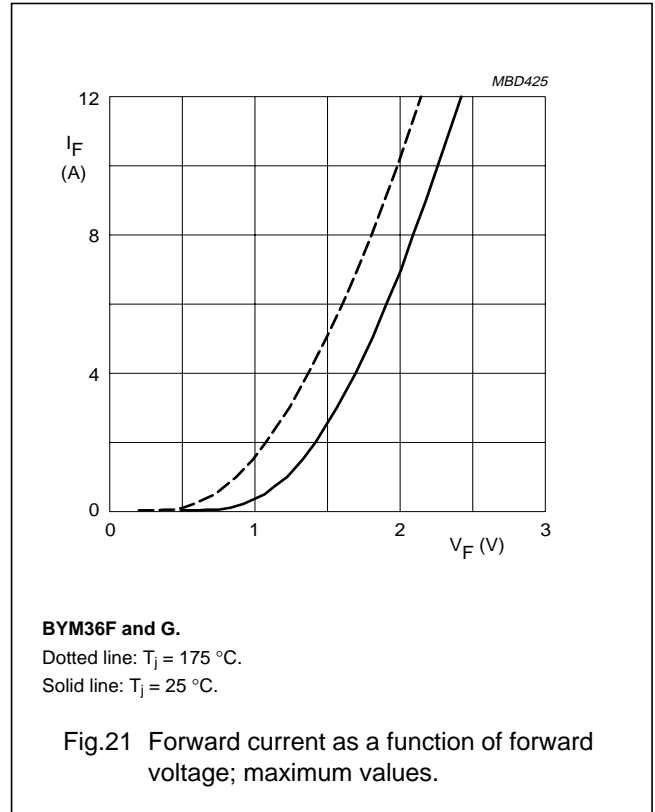
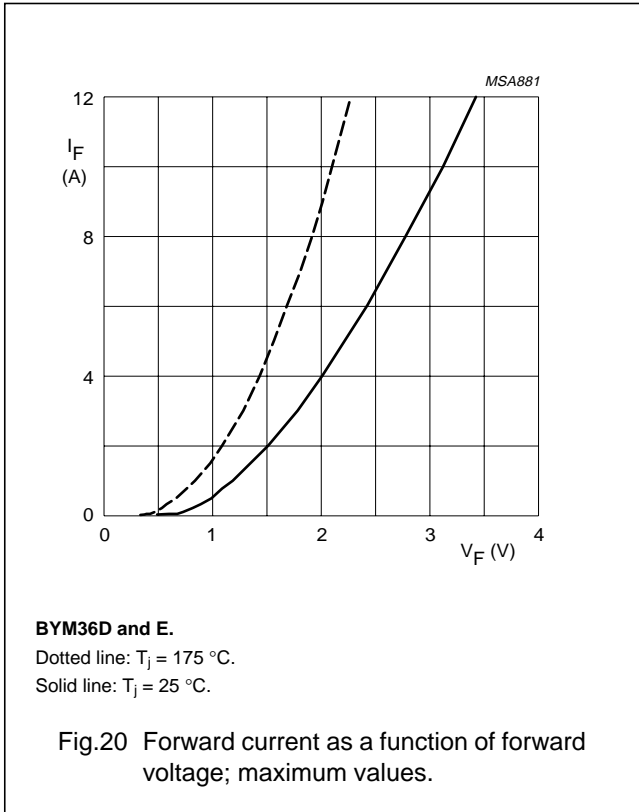
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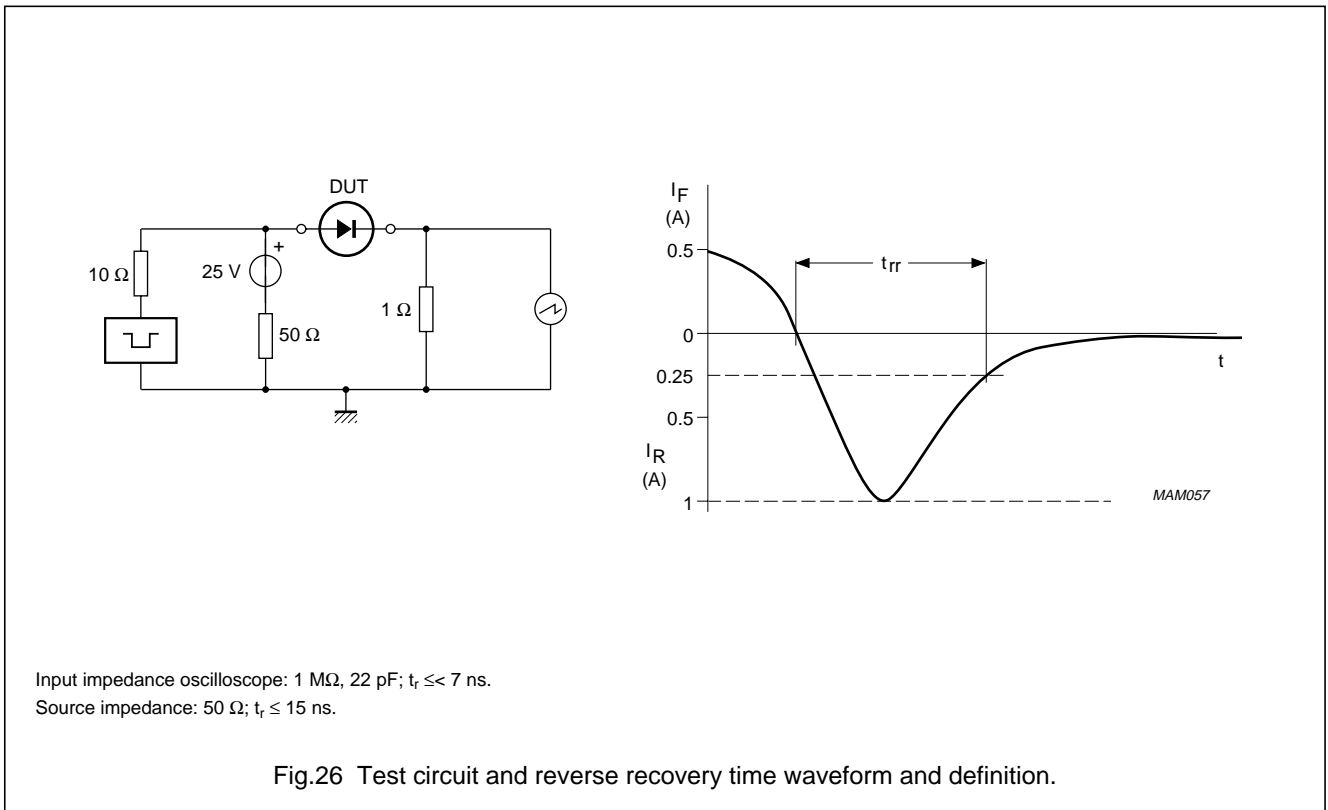
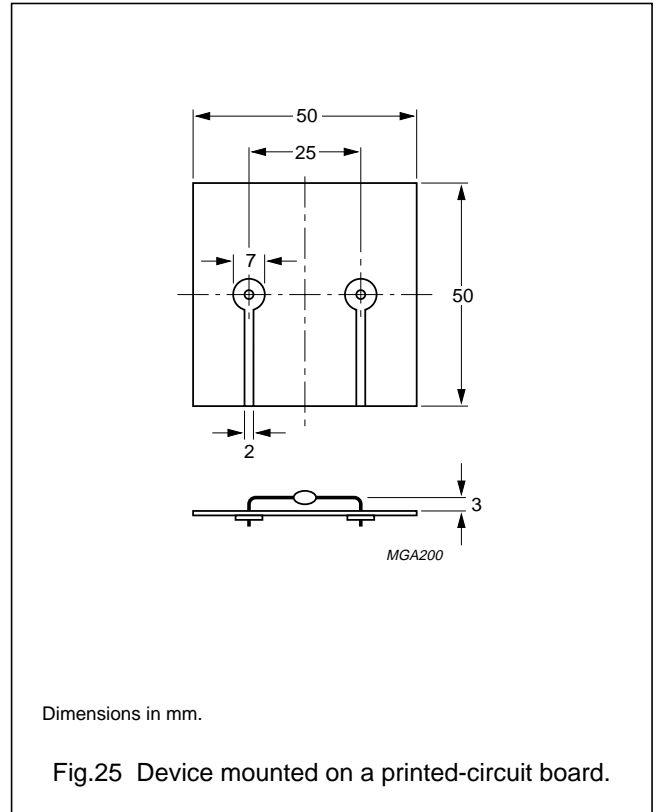
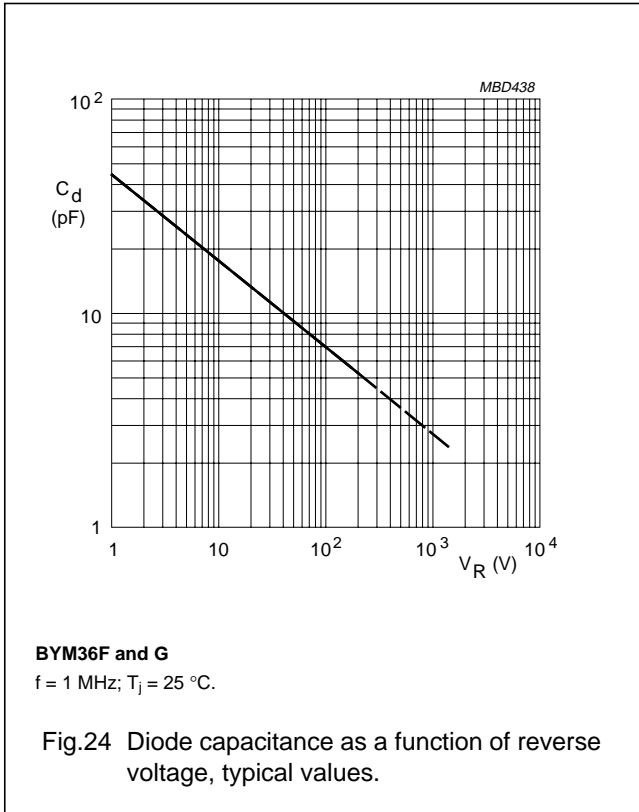
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