

## BYV36 series

### FEATURES

- Glass passivated
- High maximum operating temperature
- Low leakage current
- Excellent stability
- Guaranteed avalanche energy absorption capability
- Available in ammo-pack.

### DESCRIPTION

Rugged glass SOD57 package, using a high temperature alloyed

construction. This package is hermetically sealed and fatigue free as coefficients of expansion of all used parts are matched.



### LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>RRM</sub>	repetitive peak reverse voltage				
	BYV36A		–	200	V
	BYV36B		–	400	V
	BYV36C		–	600	V
	BYV36D		–	800	V
	BYV36E		–	1000	V
	BYV36F BYV36G		–	1200 1400	V V
V <sub>R</sub>	continuous reverse voltage				
	BYV36A		–	200	V
	BYV36B		–	400	V
	BYV36C		–	600	V
	BYV36D		–	800	V
	BYV36E		–	1000	V
	BYV36F BYV36G		–	1200 1400	V V
I <sub>F(AV)</sub>	average forward current	T <sub>tp</sub> = 60 °C; lead length = 10 mm; see Figs 2; 3 and 4	–	1.6	A
	BYV36A to C		–	1.5	A
	BYV36D and E BYV36F and G	averaged over any 20 ms period; see also Figs 14; 15 and 16	–	1.5	A
I <sub>F(AV)</sub>	average forward current	T <sub>amb</sub> = 60 °C; PCB mounting (see Fig.25); see Figs 5; 6 and 7	–	0.87	A
	BYV36A to C		–	0.81	A
	BYV36D and E BYV36F and G	averaged over any 20 ms period; see also Figs 14; 15 and 16	–	0.81	A



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SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I <sub>FRM</sub>	repetitive peak forward current	T <sub>tp</sub> = 60 °C; see Figs 8; 9 and 10	–	18	A
	BYV36A to C			17	A
	BYV36D and E BYV36F and G			15	A
I <sub>FRM</sub>	repetitive peak forward current	T <sub>amb</sub> = 60 °C; see Figs 11; 12 and 13	–	9	A
	BYV36A to C			8	A
	BYV36D and E BYV36F and G			8	A
I <sub>FSM</sub>	non-repetitive peak forward current	t = 10 ms half sine wave; T <sub>j</sub> = T <sub>jmax</sub> prior to surge; V <sub>R</sub> = V <sub>RRMmax</sub>	–	30	A
E <sub>RSM</sub>	non-repetitive peak reverse avalanche energy	L = 120 mH; T <sub>j</sub> = T <sub>jmax</sub> prior to surge; inductive load switched off	–	10	mJ
T <sub>stg</sub>	storage temperature		–65	+175	°C
T <sub>j</sub>	junction temperature	see Figs 17 and 18	–65	+175	°C

#### ELECTRICAL CHARACTERISTICS

T<sub>j</sub> = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT			
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 1 A; T <sub>j</sub> = T <sub>jmax</sub> ; see Figs 19; 20 and 21	–	–	1.00	V			
	BYV36A to C				1.05	V			
	BYV36D and E BYV36F and G				1.05	V			
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 1 A; see Figs 19; 20 and 21	–	–	1.35	V			
	BYV36A to C				1.45	V			
	BYV36D and E BYV36F and G				1.45	V			
V <sub>(BR)R</sub>	reverse avalanche breakdown voltage	I <sub>R</sub> = 0.1 mA							
	BYV36A					300	–	–	V
	BYV36B					500	–	–	V
	BYV36C					700	–	–	V
	BYV36D					900	–	–	V
	BYV36E					1100	–	–	V
	BYV36F					1300	–	–	V
BYV36G	1500	–	–	V					
I <sub>R</sub>	reverse current	V <sub>R</sub> = V <sub>RRMmax</sub> ; see Fig.22	–	–	5	μA			
		V <sub>R</sub> = V <sub>RRMmax</sub> ; T <sub>j</sub> = 165 °C; see Fig.22	–	–	150	μA			



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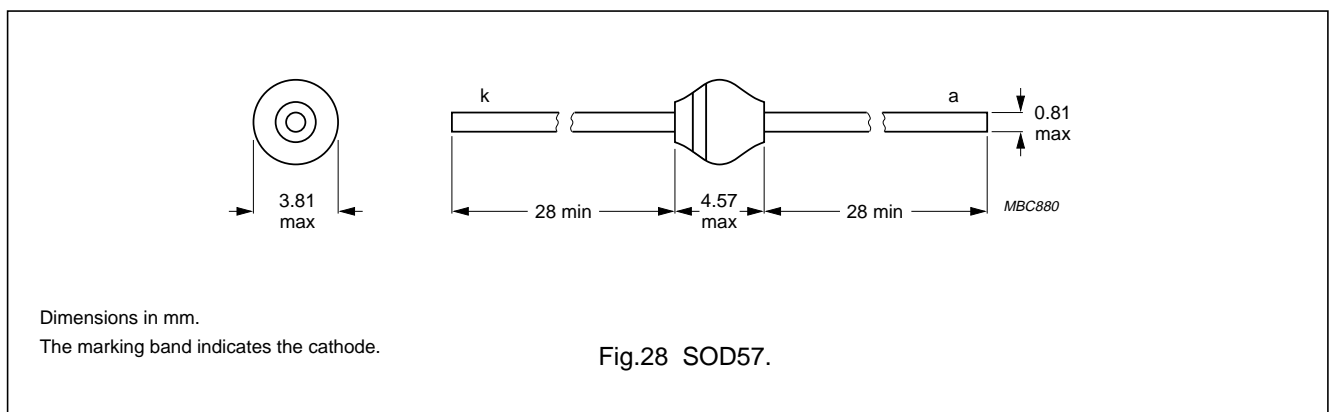
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$t_{rr}$	reverse recovery time	when switched from				
	BYV36A to C	$I_F = 0.5 \text{ A}$ to $I_R = 1 \text{ A}$ ;	–	–	100	ns
	BYV36D and E	measured at $I_R = 0.25 \text{ A}$ ;	–	–	150	ns
	BYV36F and G	see Fig. 26	–	–	250	ns
$C_d$	diode capacitance	$f = 1 \text{ MHz}$ ; $V_R = 0 \text{ V}$ ;				
	BYV36A to C	see Figs 23 and 24	–	45	–	pF
	BYV36D and E		–	40	–	pF
	BYV36F and G		–	35	–	pF
$\left  \frac{dI_R}{dt} \right $	maximum slope of reverse recovery current	when switched from				
	BYV36A to C	$I_F = 1 \text{ A}$ to $V_R \geq 30 \text{ V}$ and	–	–	7	A/ $\mu\text{s}$
	BYV36D and E	$dI_F/dt = -1 \text{ A}/\mu\text{s}$ ;	–	–	6	A/ $\mu\text{s}$
	BYV36F and G	see Fig.27	–	–	5	A/ $\mu\text{s}$

### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th\ j-tp}$	thermal resistance from junction to tie-point	lead length = 10 mm	46	K/W
$R_{th\ j-a}$	thermal resistance from junction to ambient	note 1	100	K/W

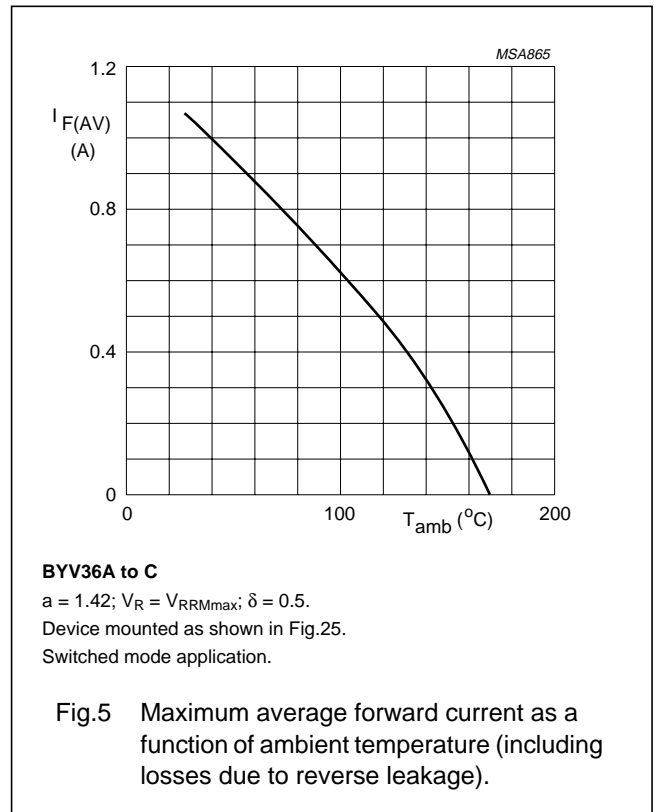
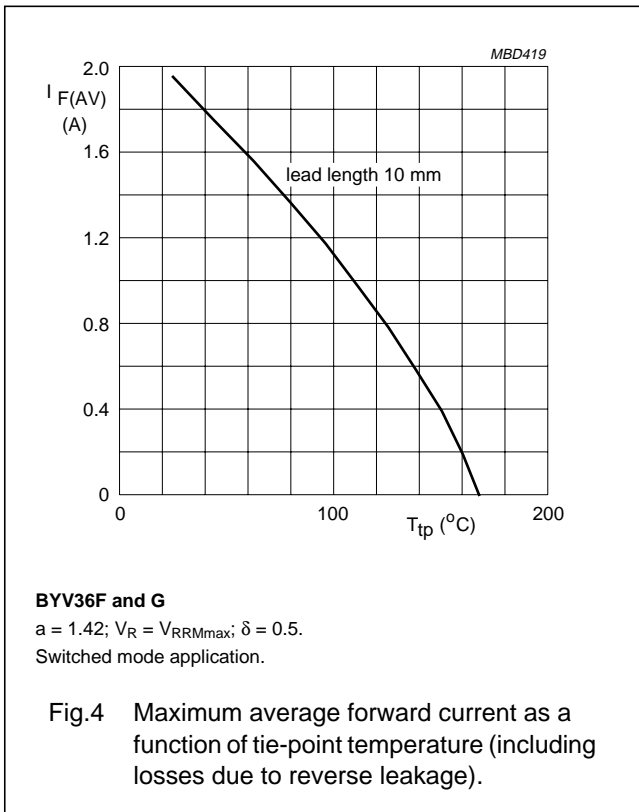
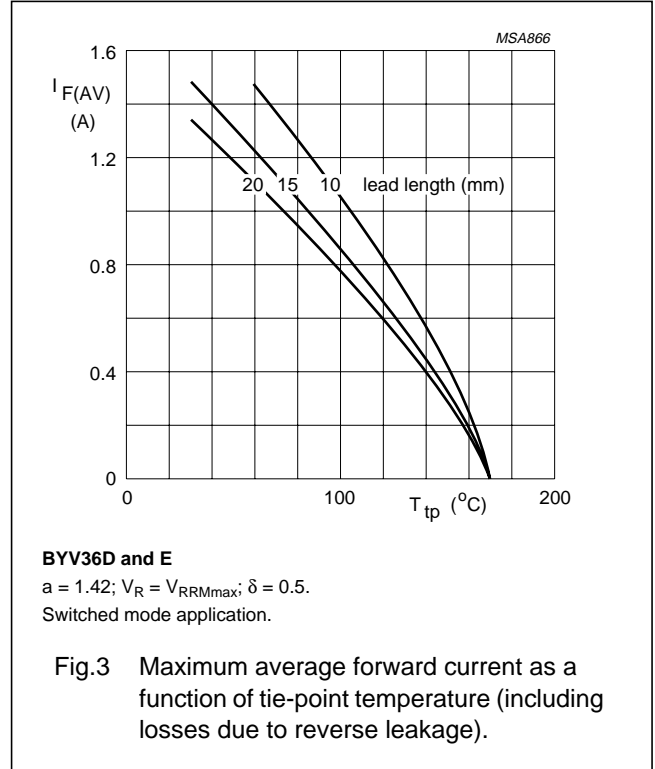
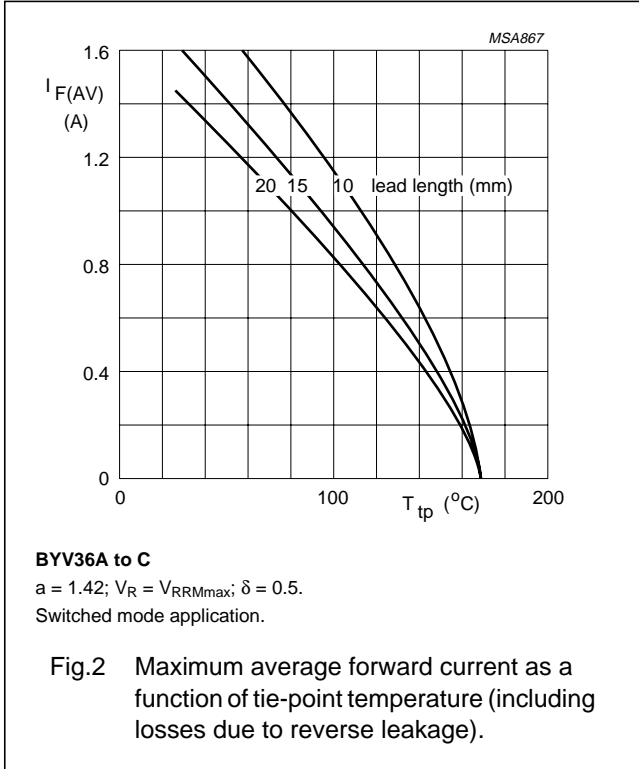
### Note

1. Device mounted on an epoxy-glass printed-circuit board, 1.5 mm thick; thickness of Cu-layer  $\geq 40 \mu\text{m}$ , see Fig.25. For more information please refer to the "General Part of associated Handbook".

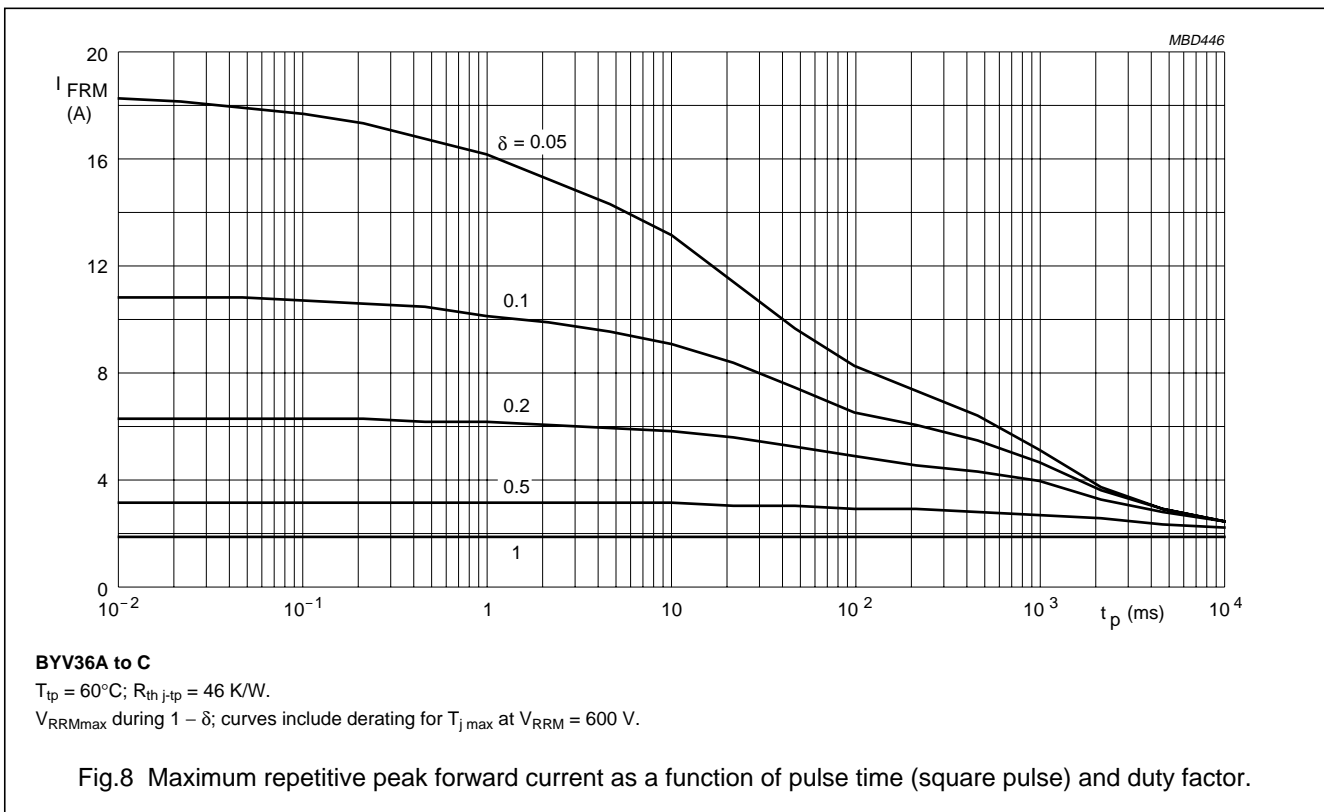
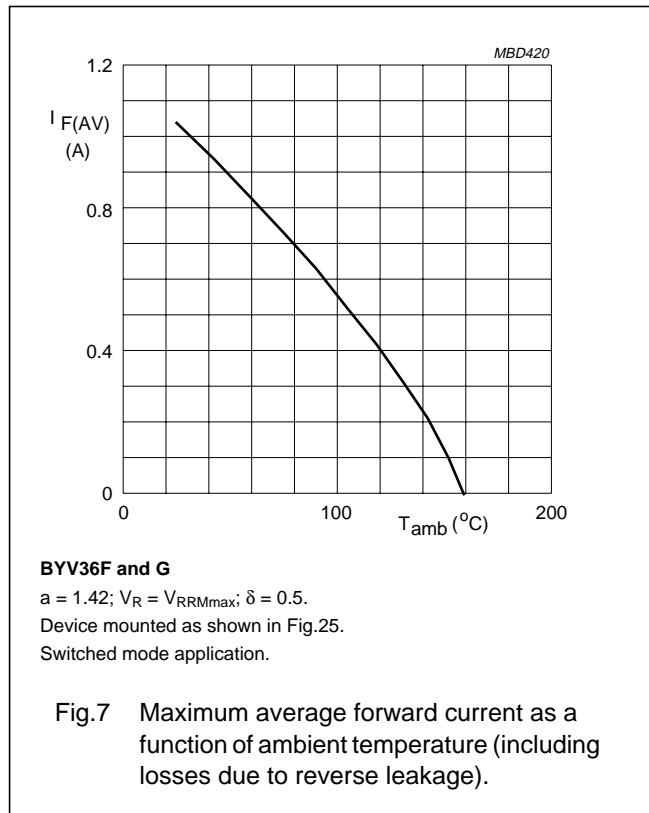
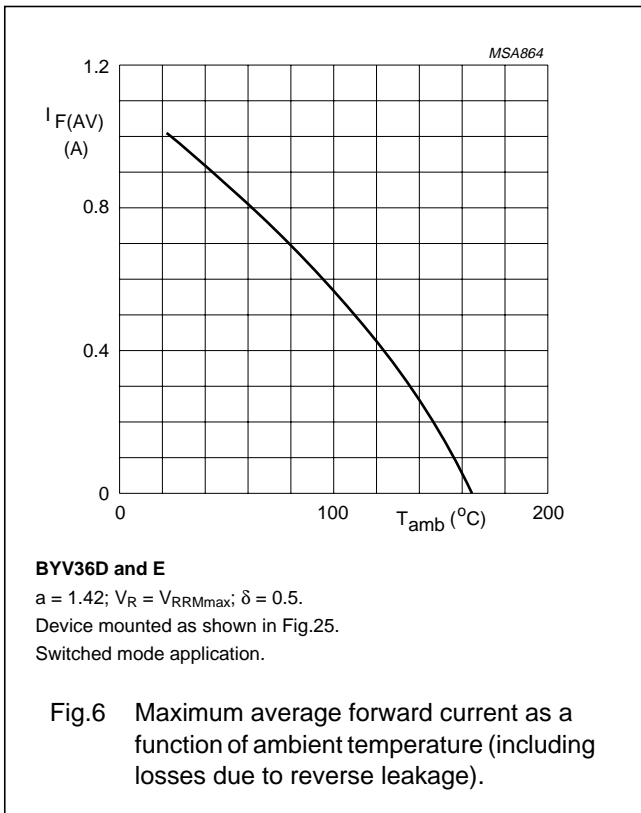


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GRAPHICAL DATA



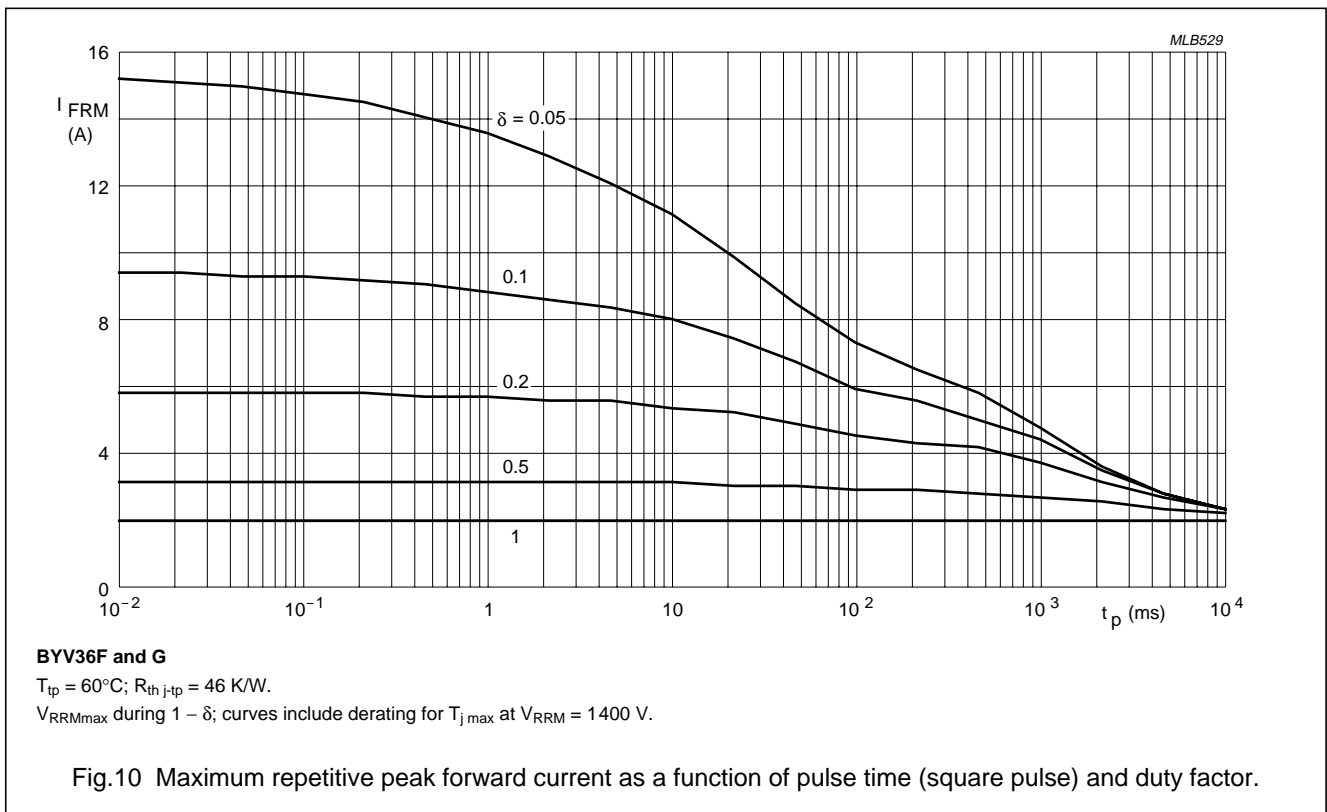
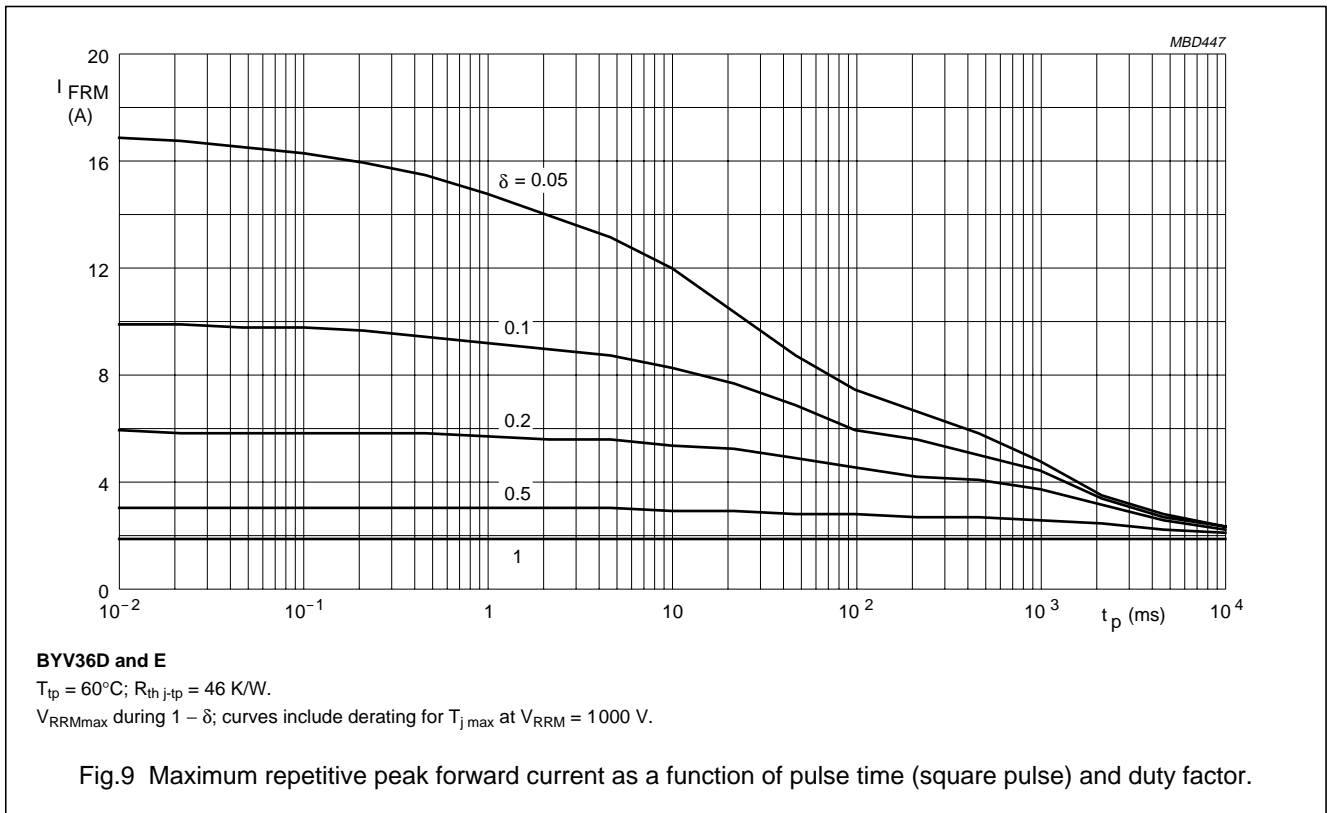
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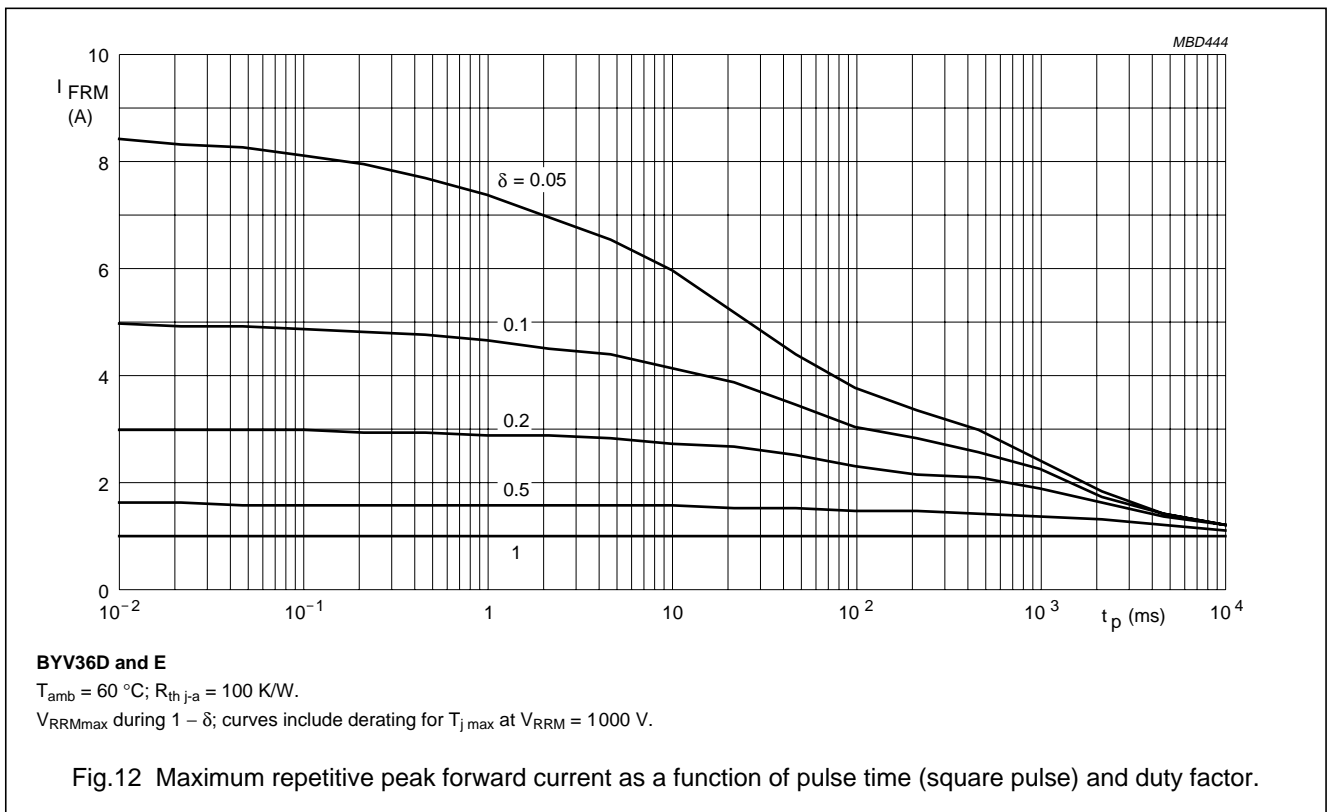
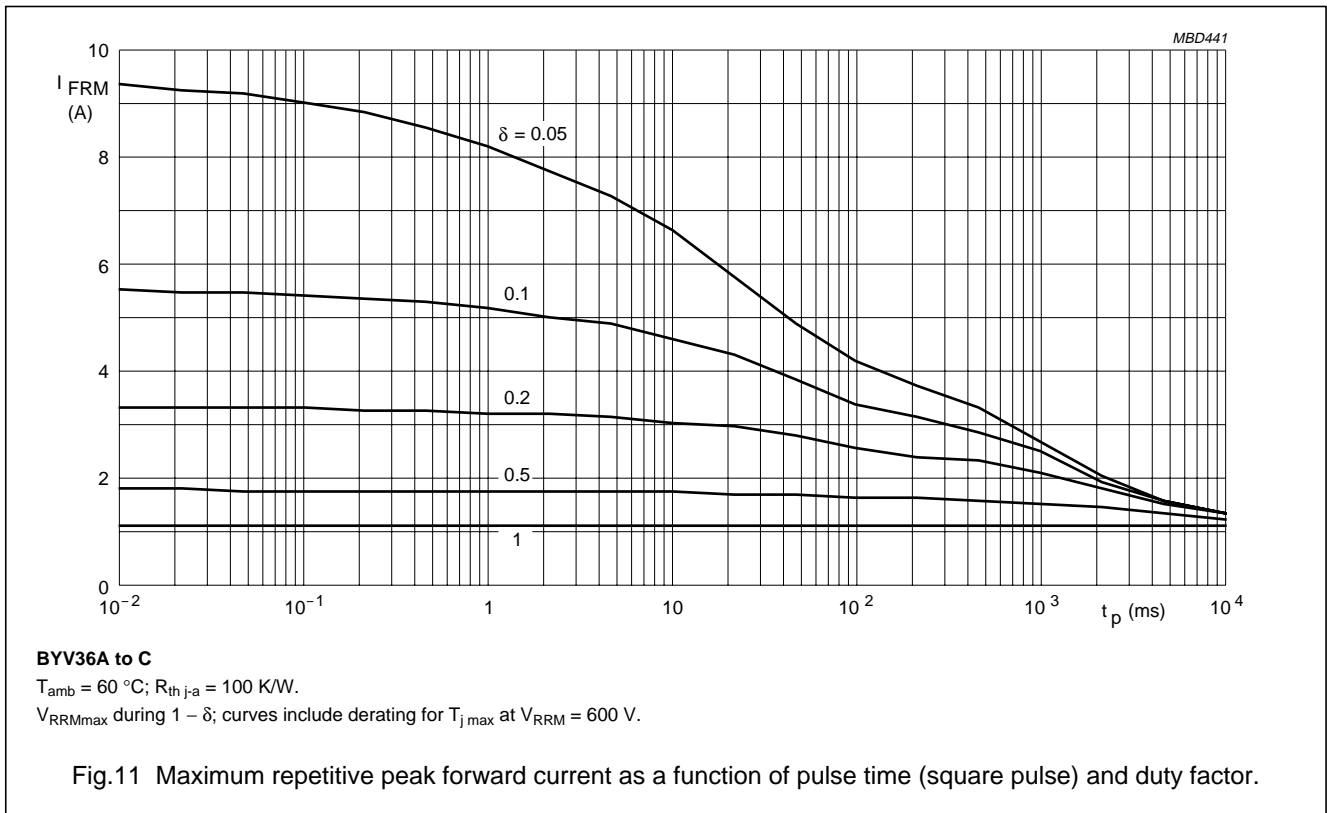


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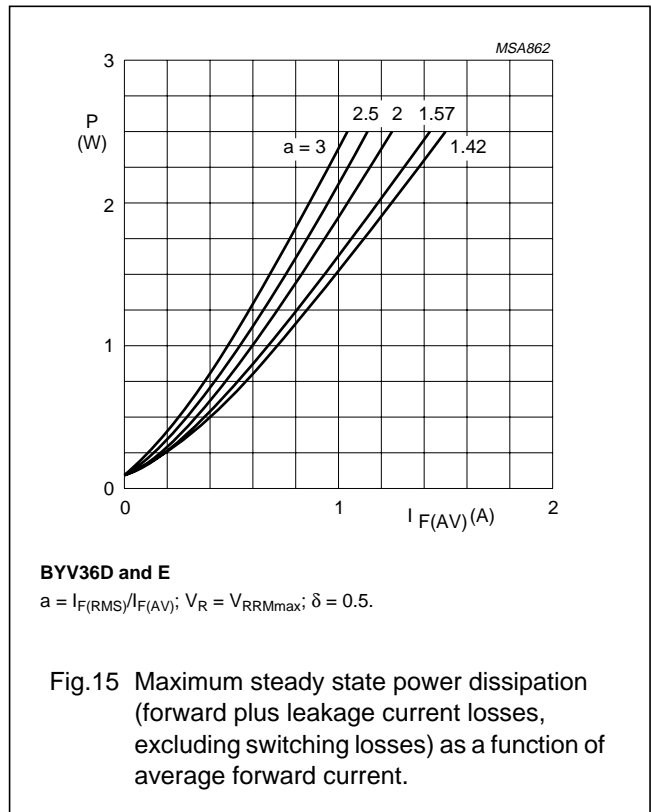
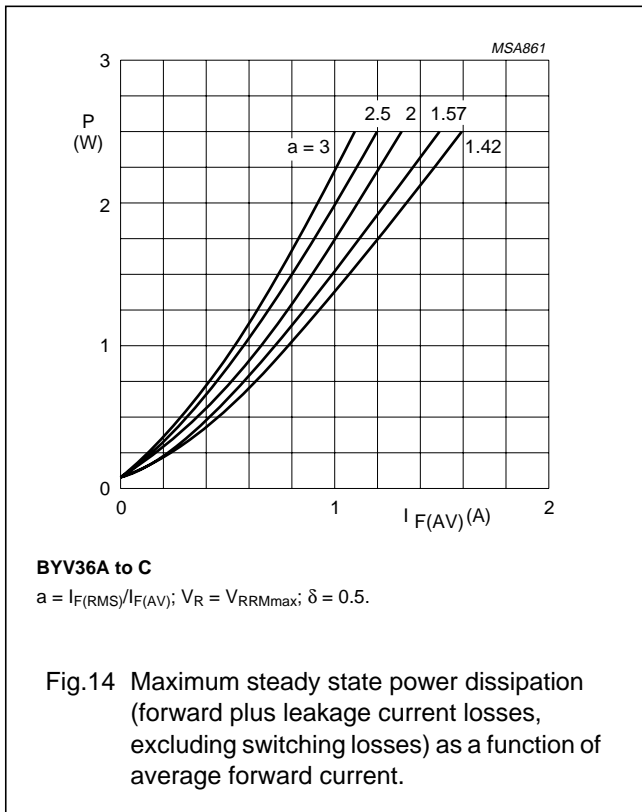
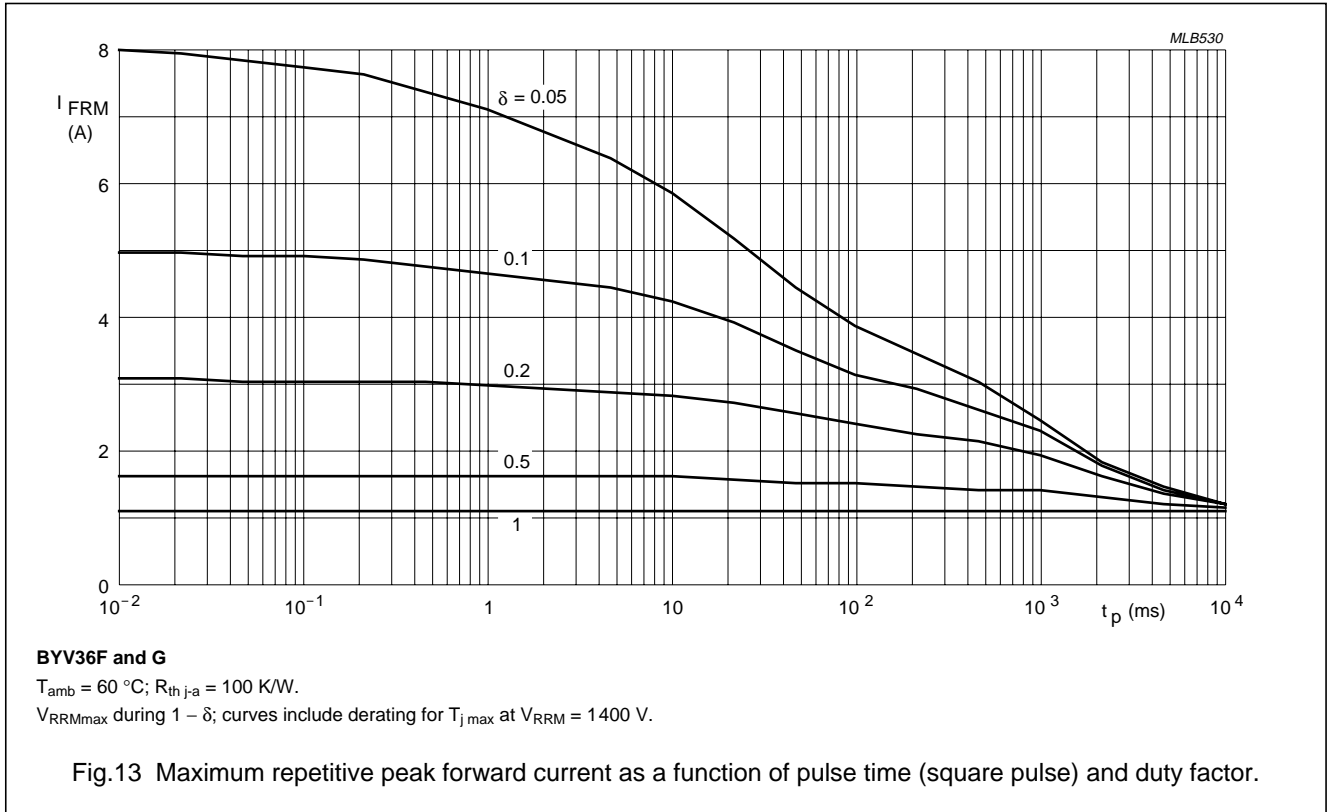
### BYV36 series



BYV36 series

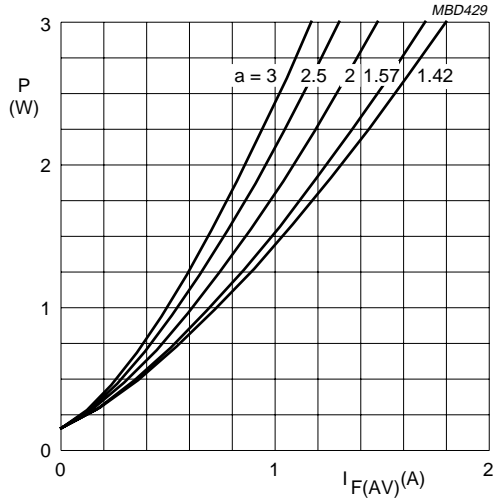


## BYV36 series





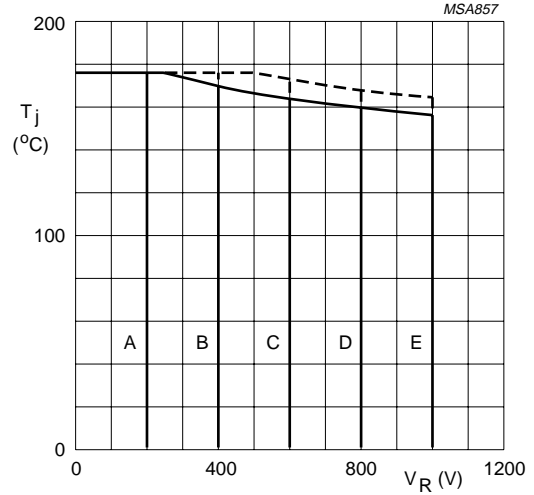
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#### BYV36F and G

$$a = I_{F(RMS)} / I_{F(AV)}; V_R = V_{RRMmax}; \delta = 0.5.$$

Fig.16 Maximum steady state power dissipation (forward plus leakage current losses, excluding switching losses) as a function of average forward current.

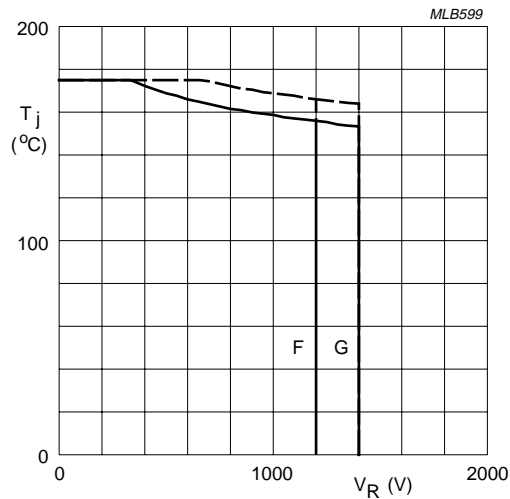


#### BYV36A to E

Solid line =  $V_R$ .

Dotted line =  $V_{RRM}$ ;  $\delta = 0.5$ .

Fig.17 Maximum permissible junction temperature as a function of reverse voltage.

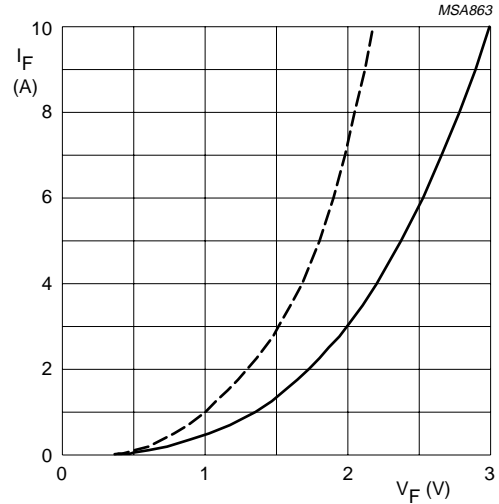


#### BYV36F and G

Solid line =  $V_R$ .

Dotted line =  $V_{RRM}$ ;  $\delta = 0.5$ .

Fig.18 Maximum permissible junction temperature as a function of reverse voltage.



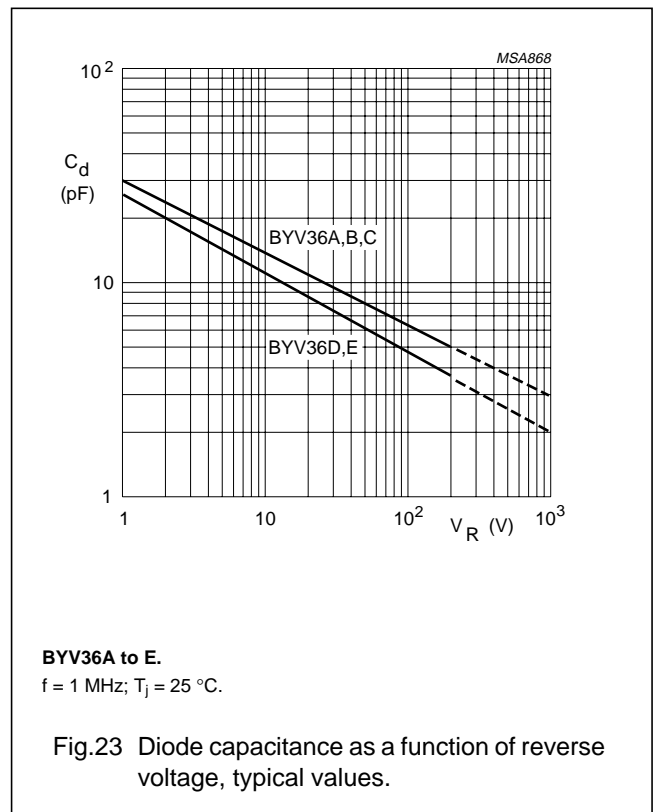
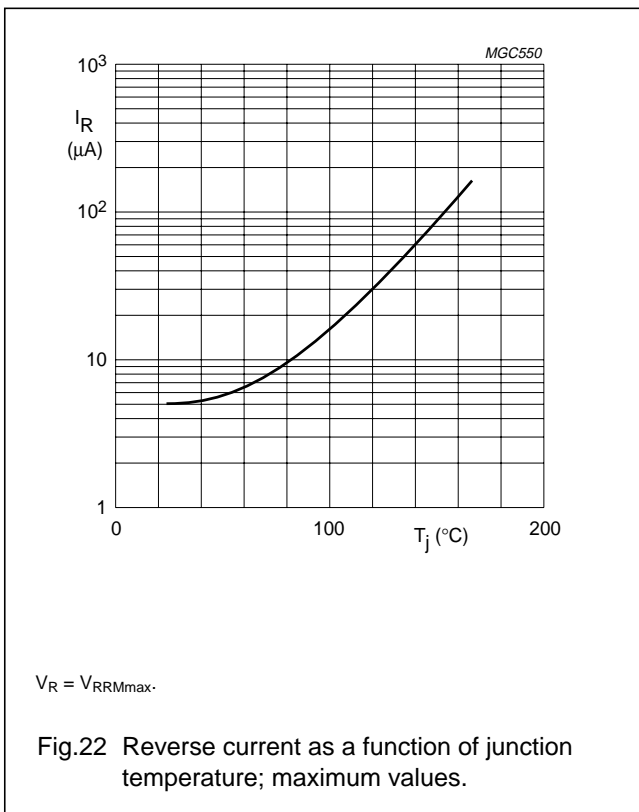
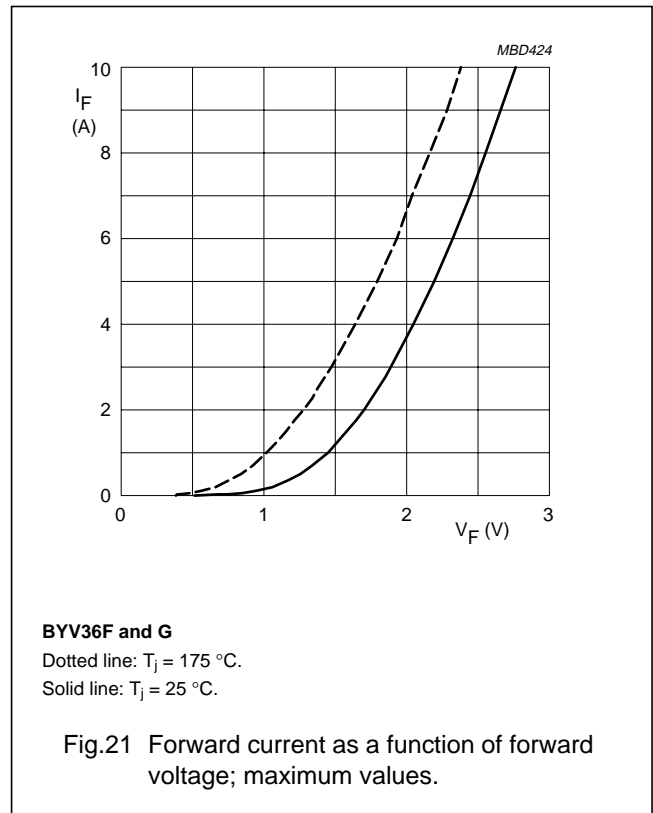
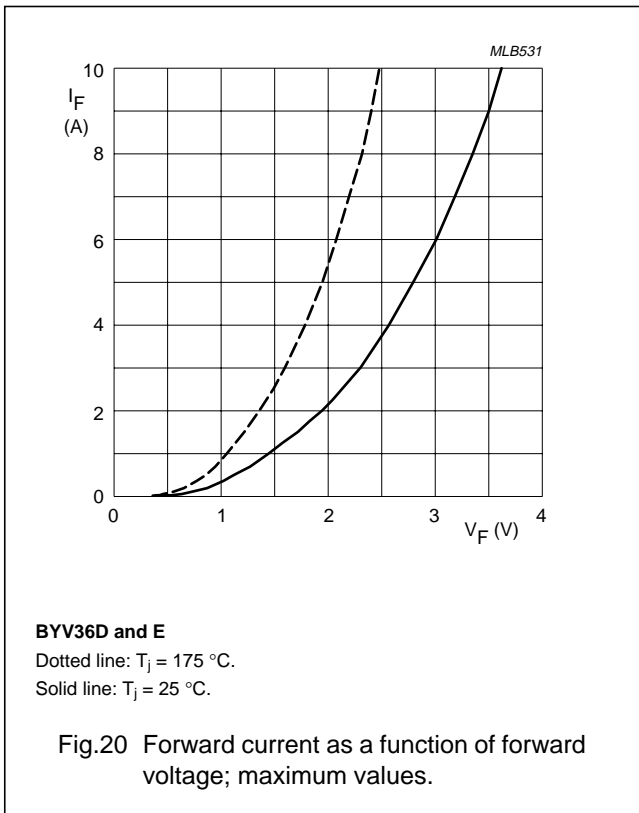
#### BYV36A to C

Dotted line:  $T_j = 175 \text{ }^\circ\text{C}$ .

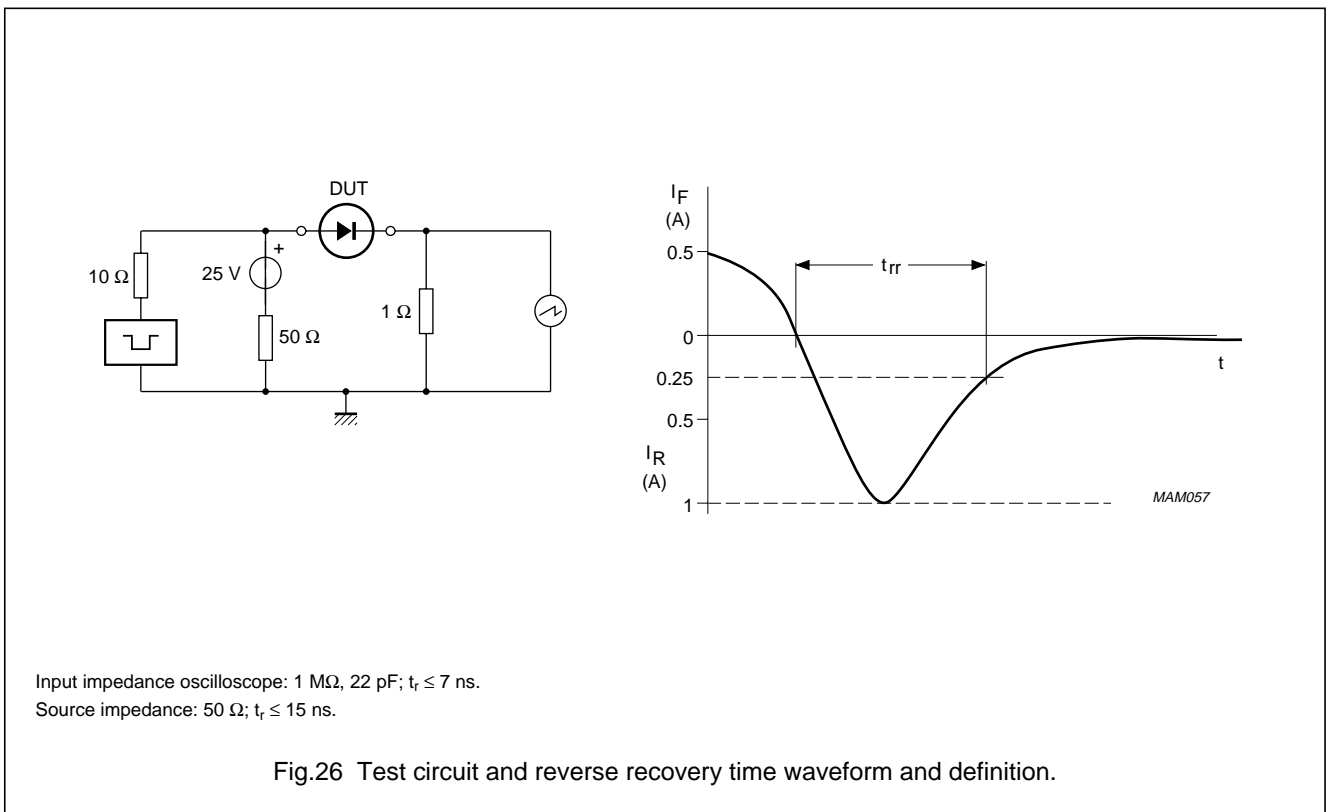
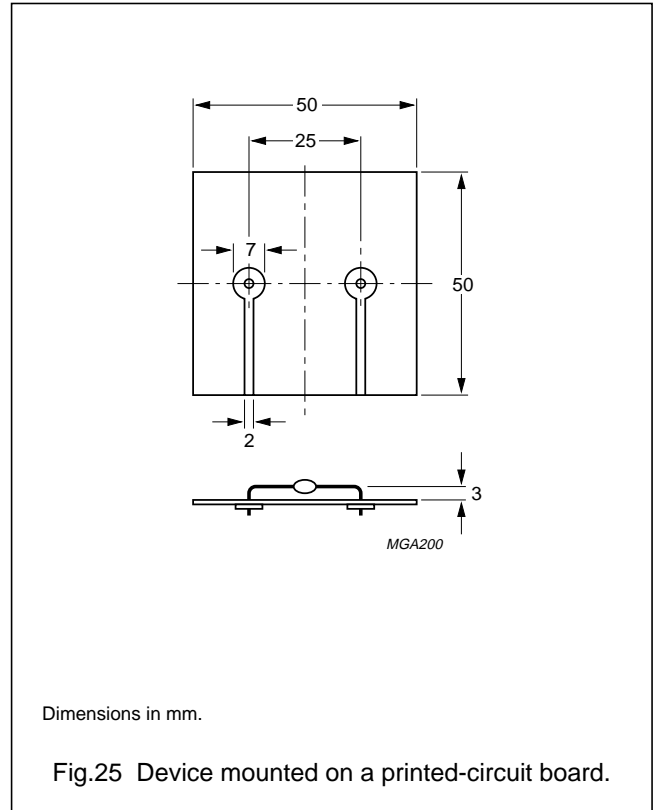
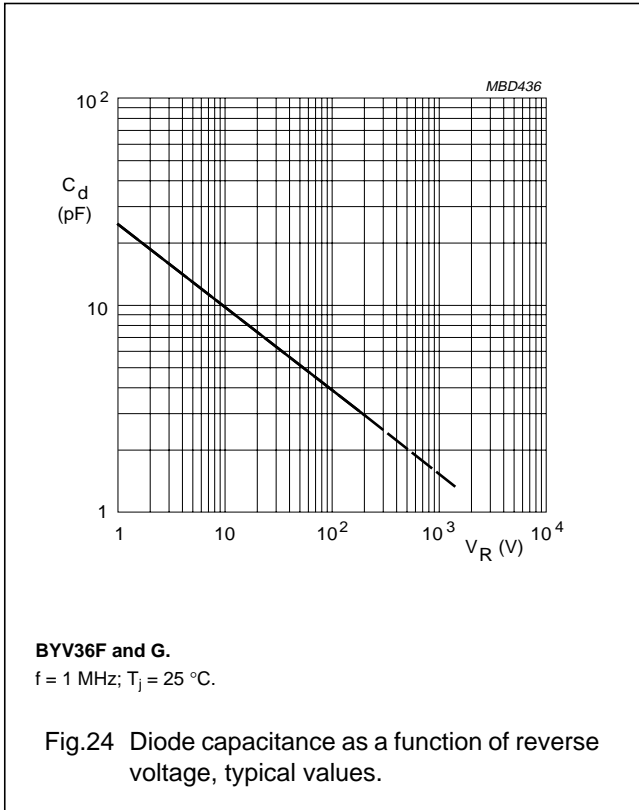
Solid line:  $T_j = 25 \text{ }^\circ\text{C}$ .

Fig.19 Forward current as a function of forward voltage; maximum values.

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